

1/52

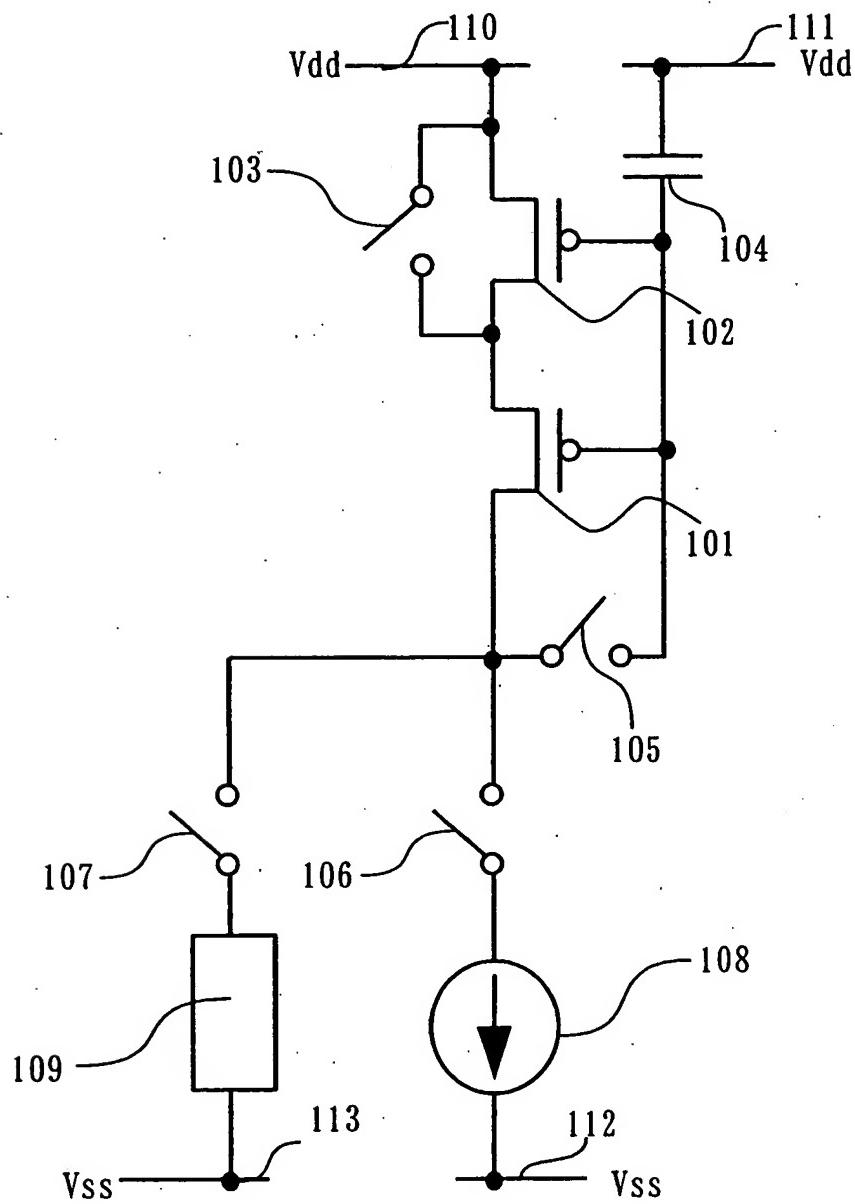


図 1

2/52

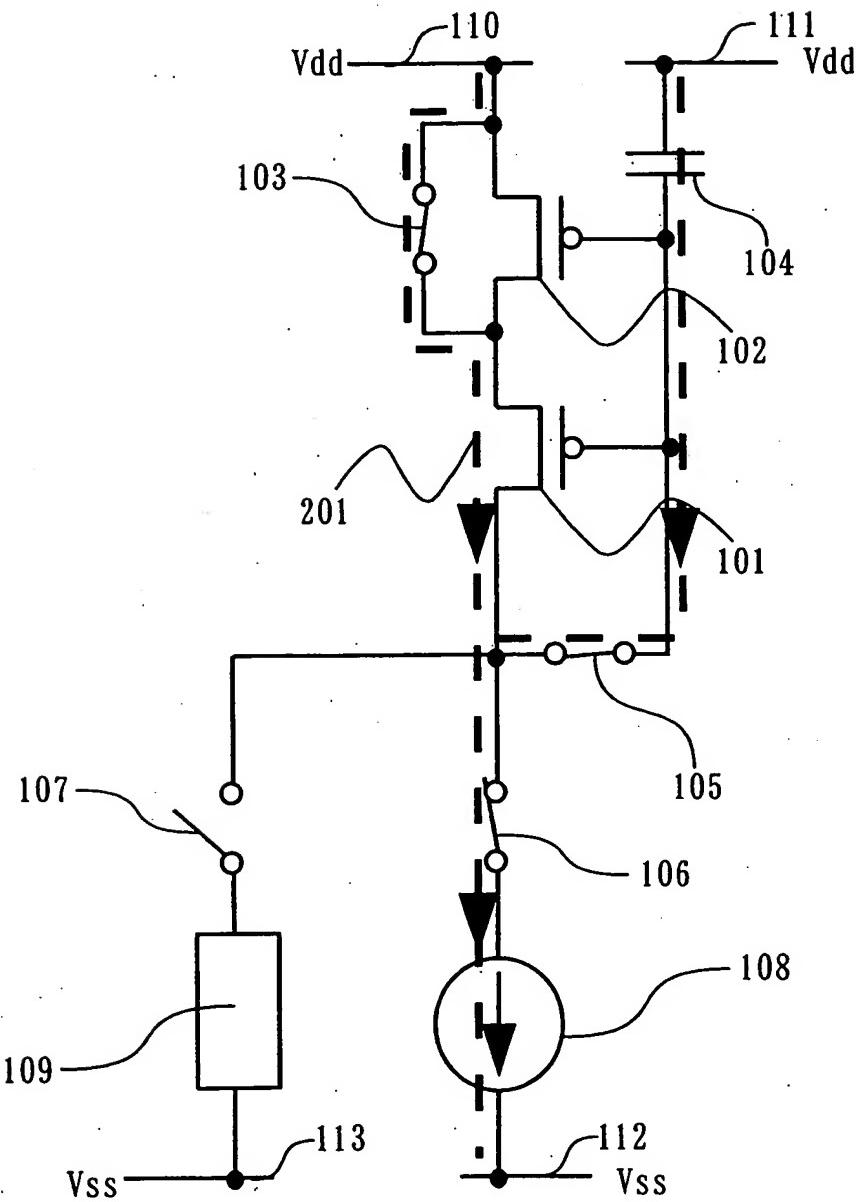


図2

3/52

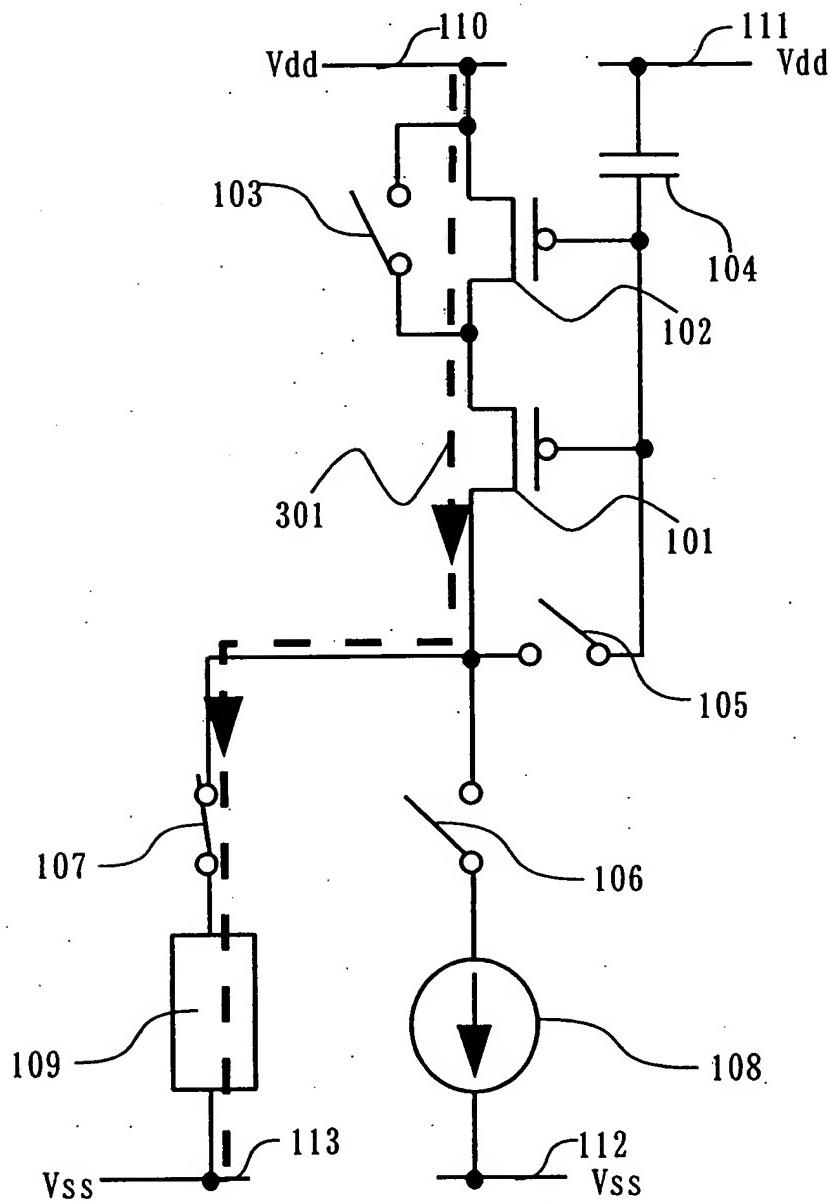


図3

4/52

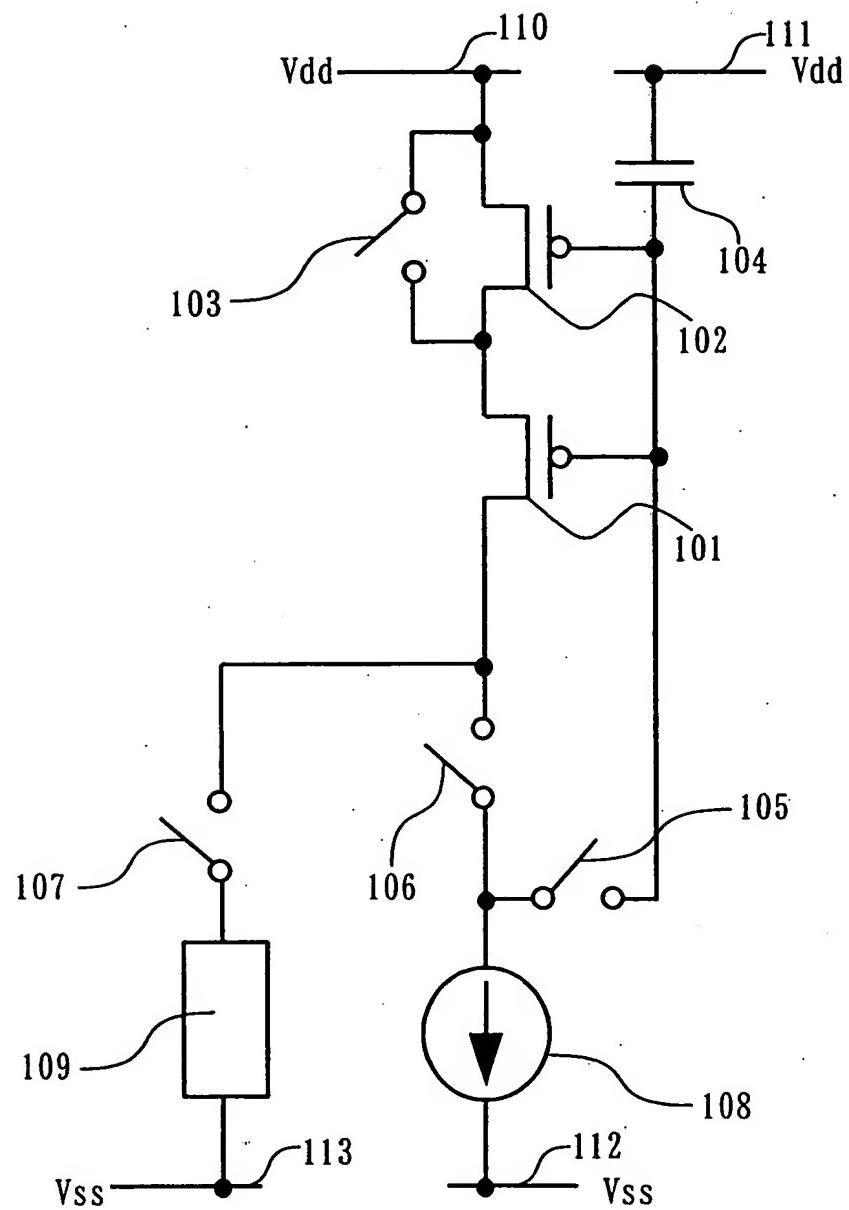


図4

5/52

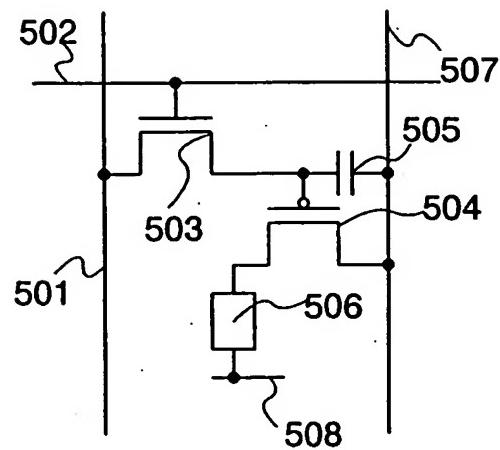


図 5

6/52

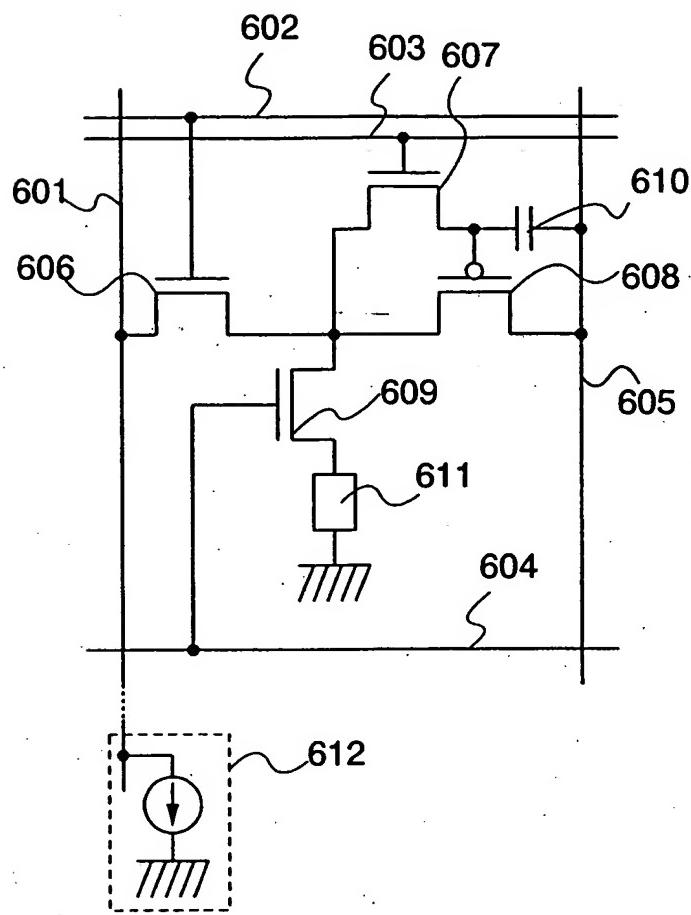
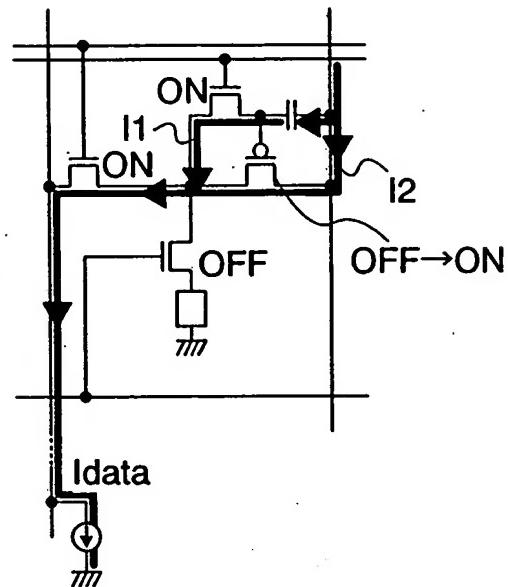


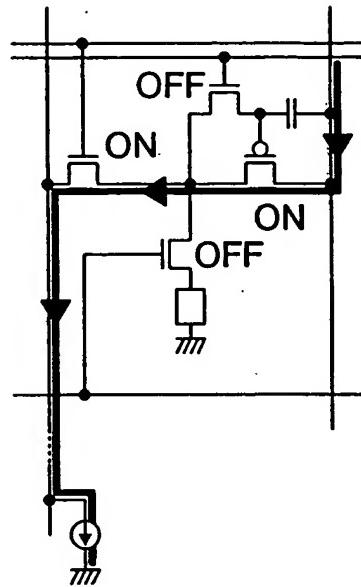
図 6

7/52

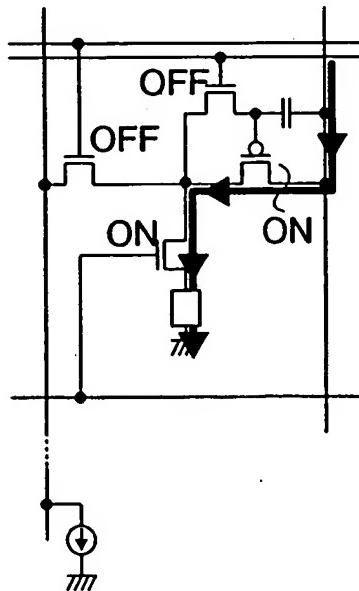
(A) 信号入力時



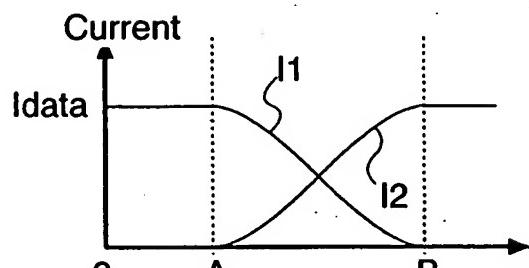
(B) 信号入力完了時



(C) 発光時



(D)



(E)

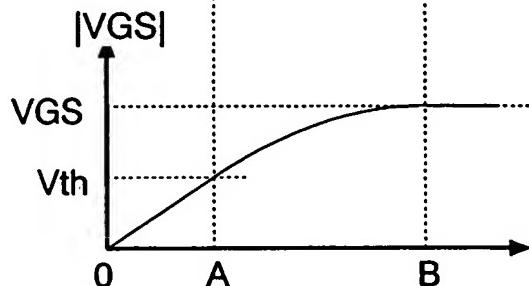


図 7

8/52

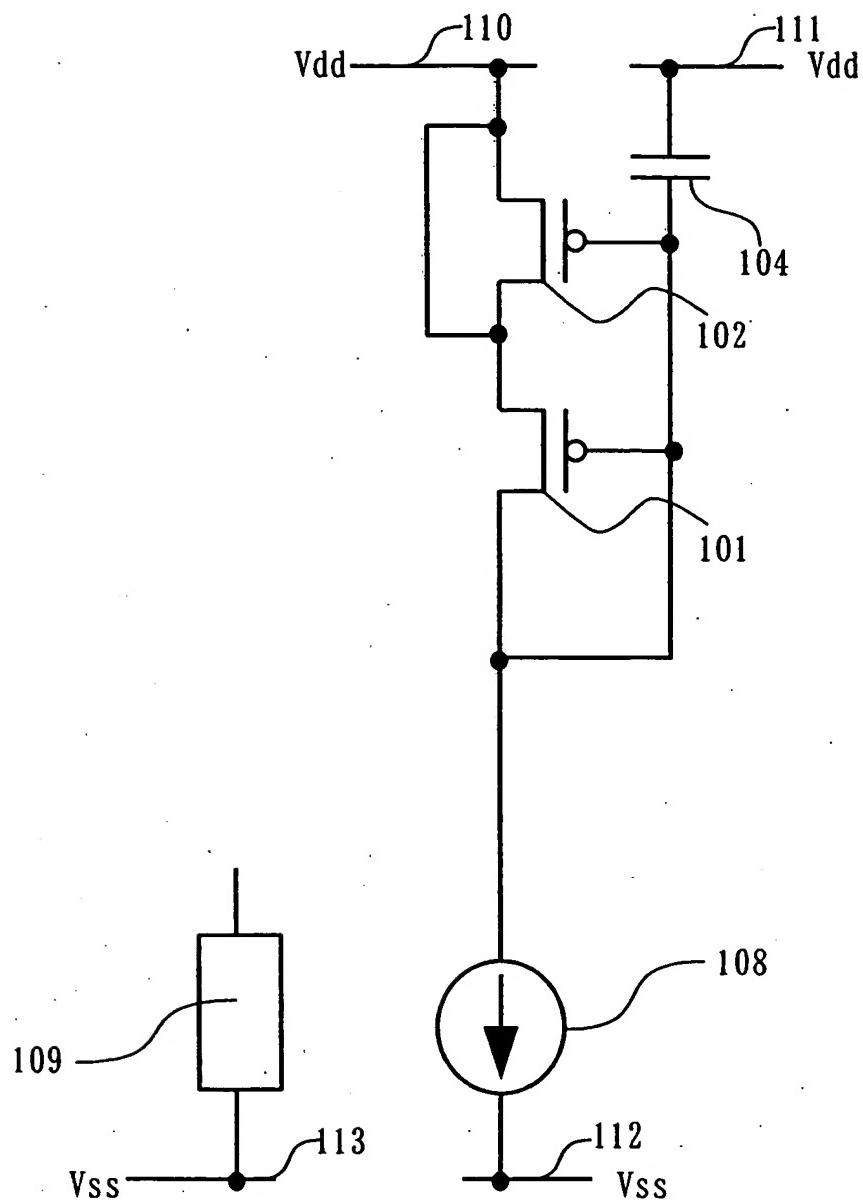


図8

9/52

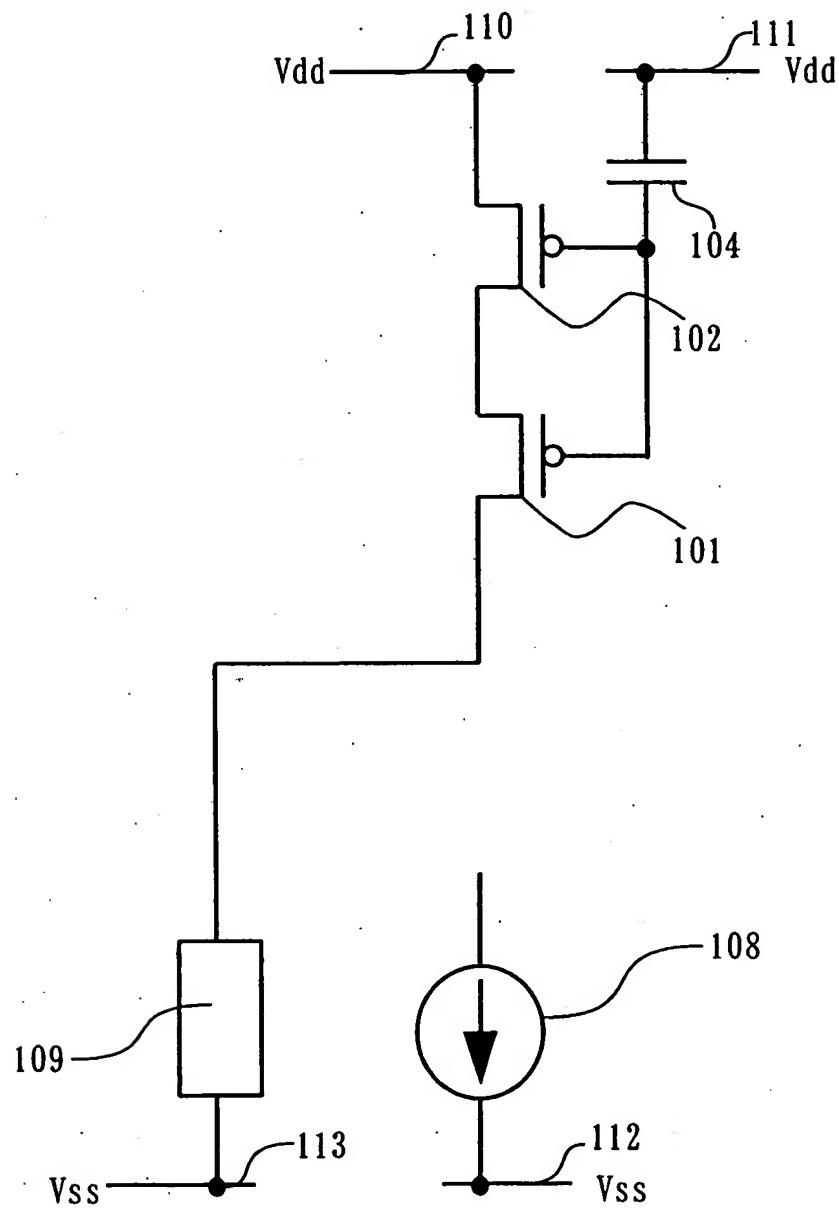


図9

10/52

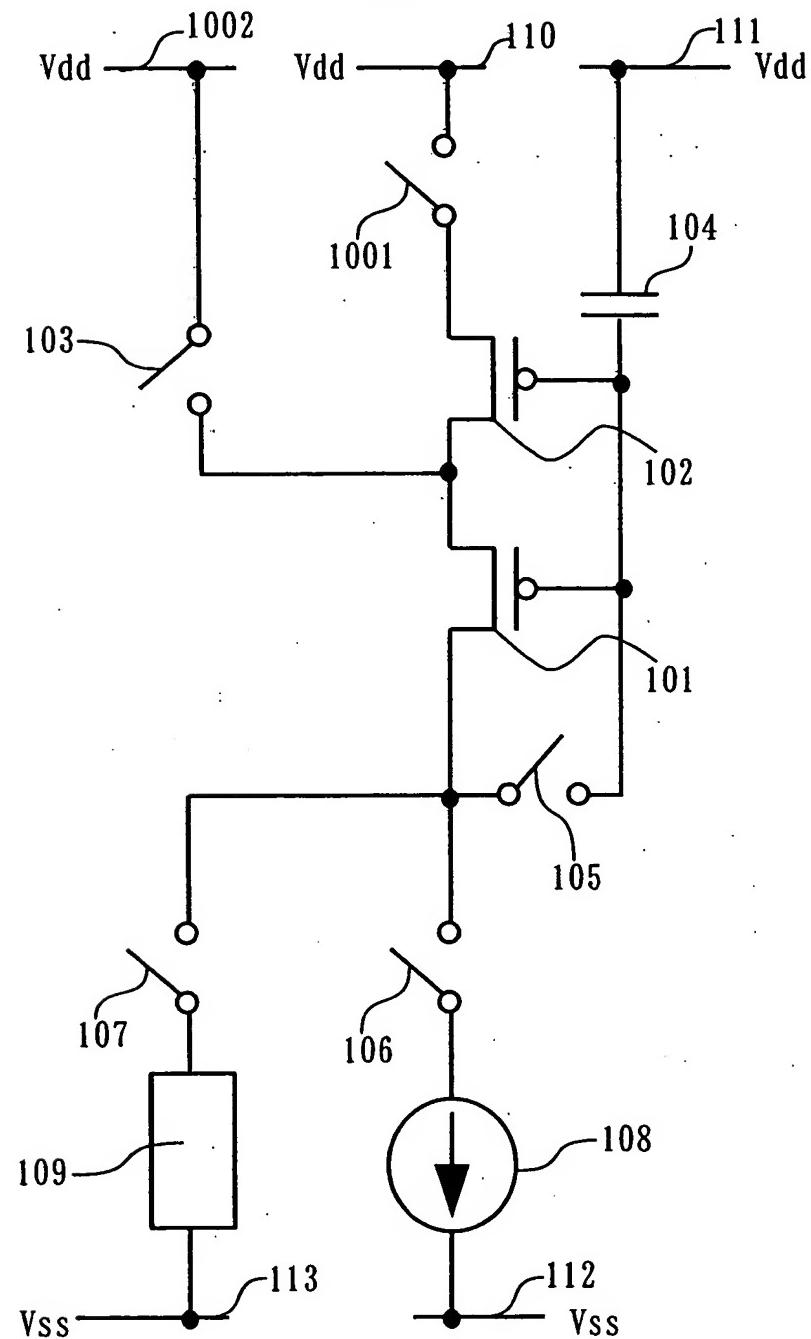


図10

11/52

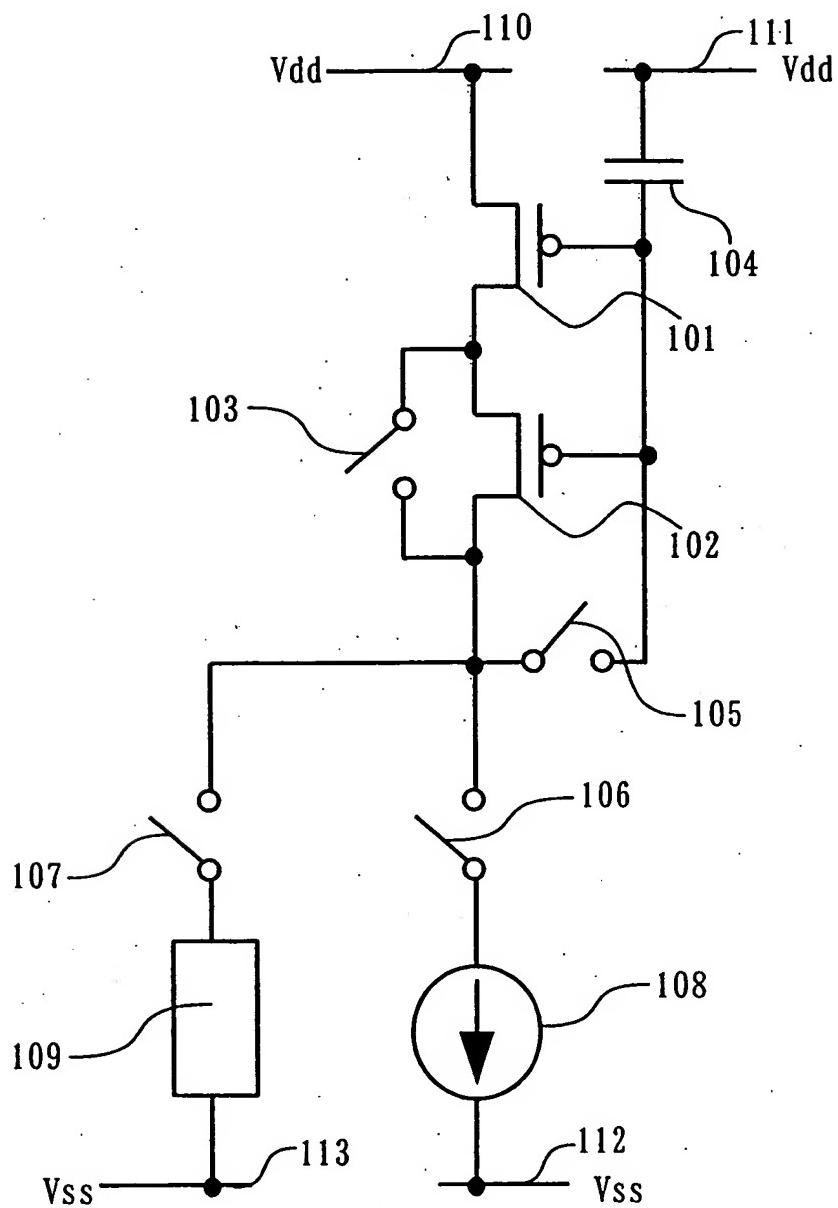


図 11

12/52

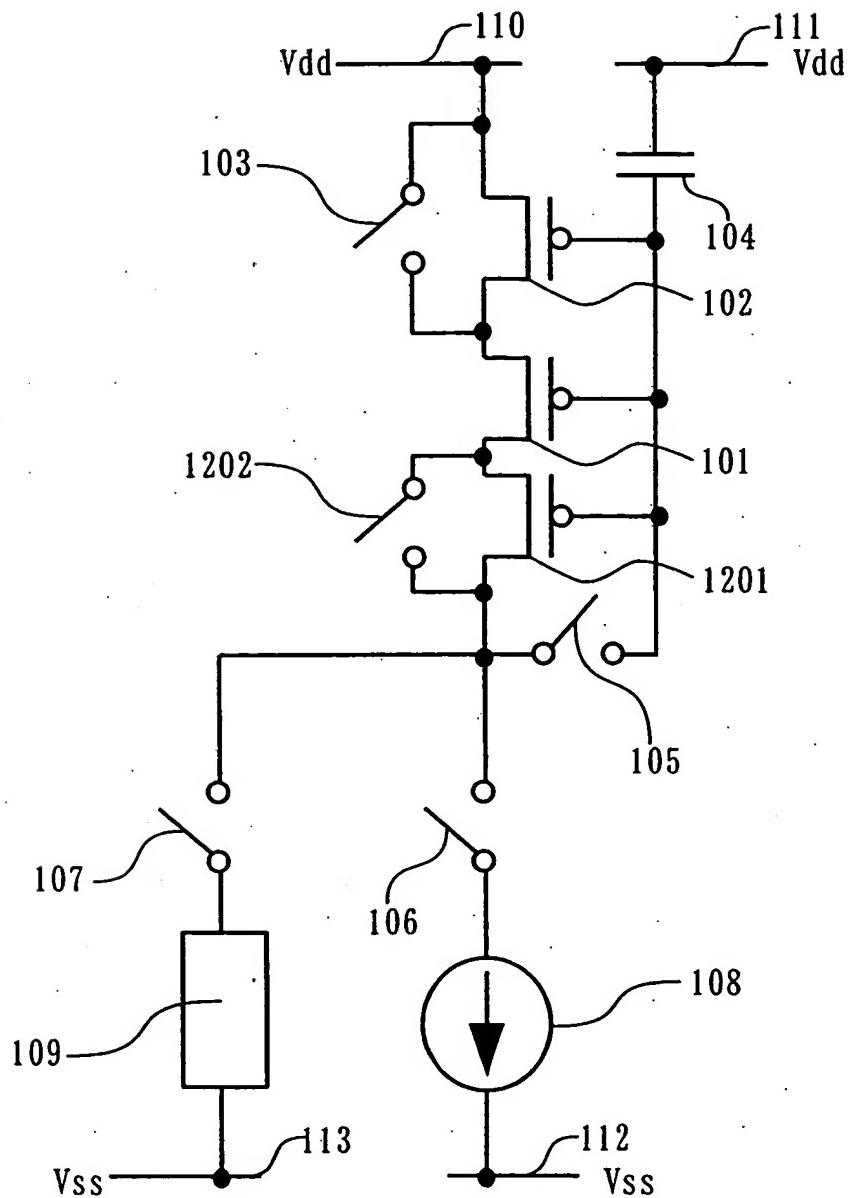


図12

13/52

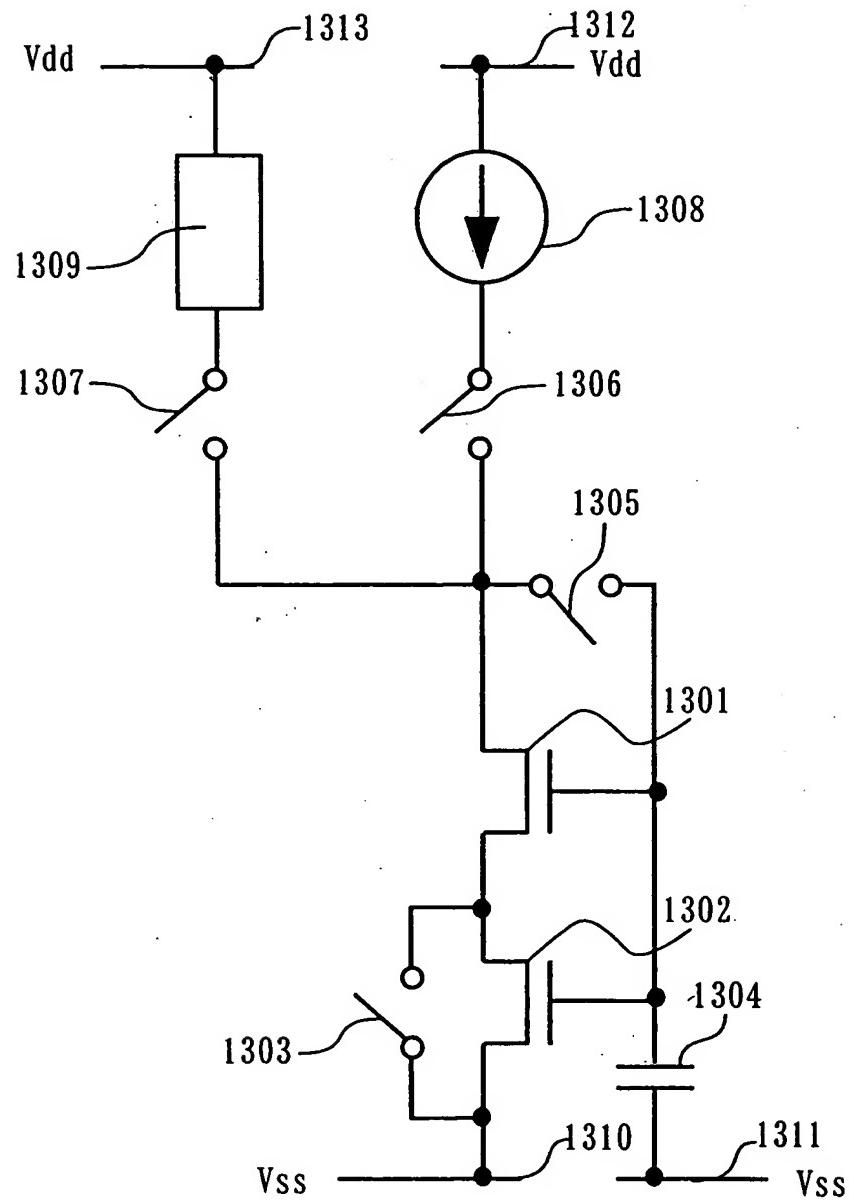


図 13

14/52

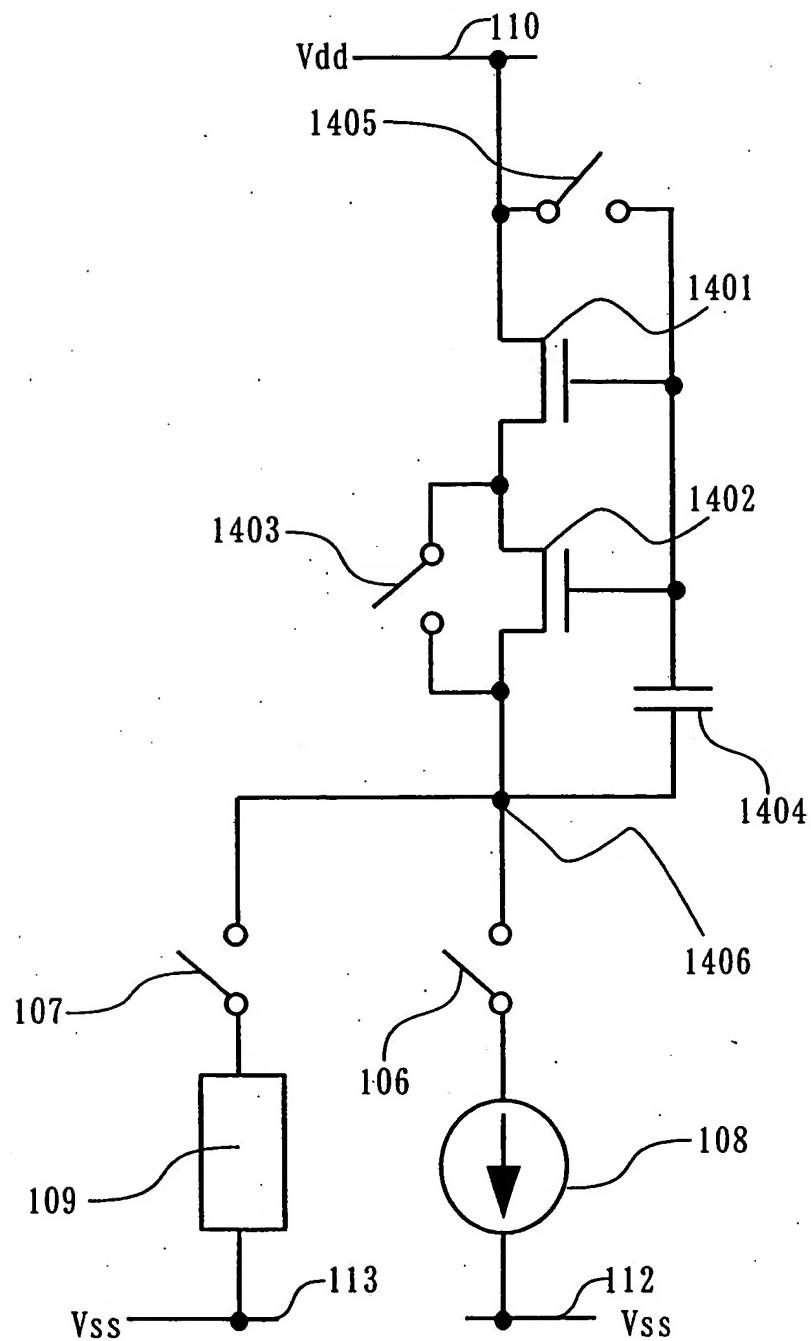


図 14

15/52

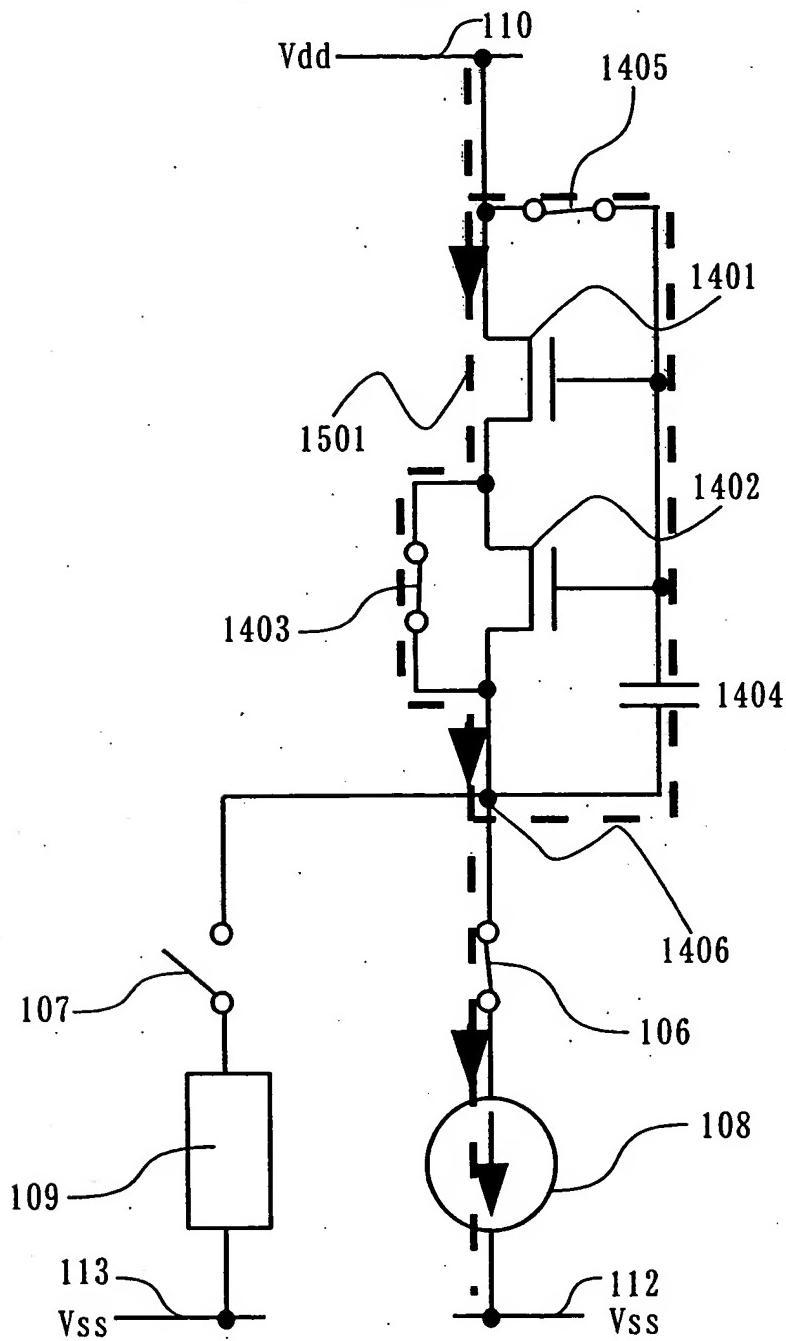


図 15

16/52

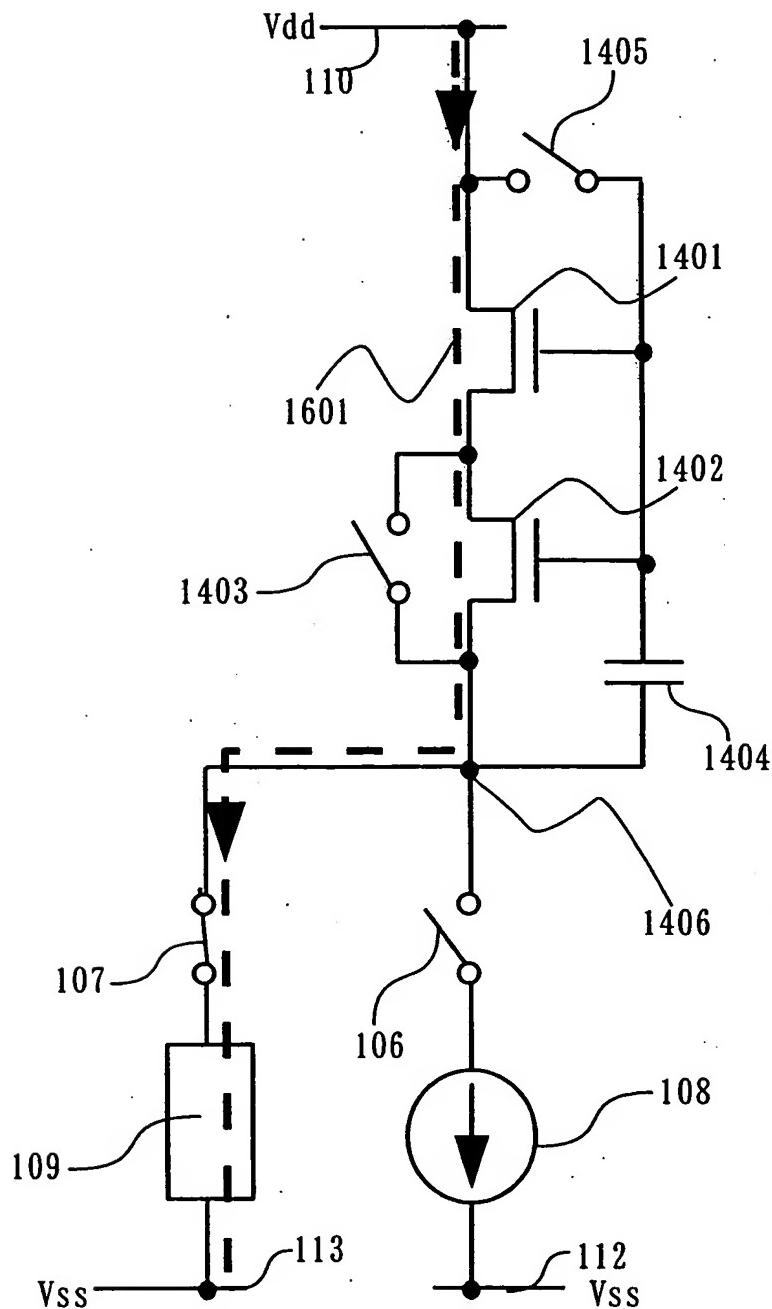


図 16

17/52

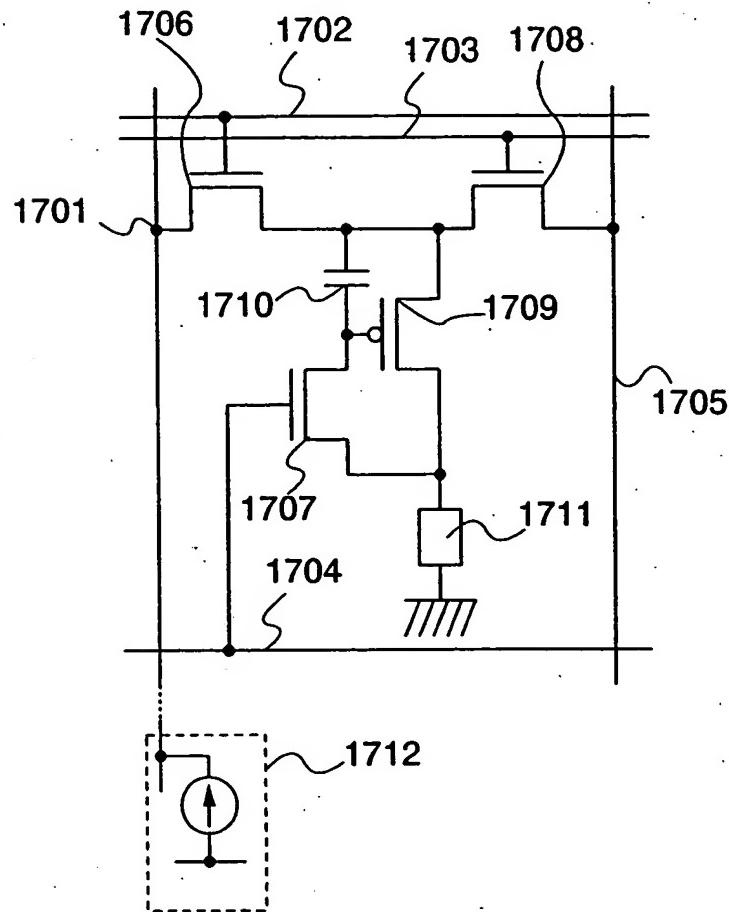
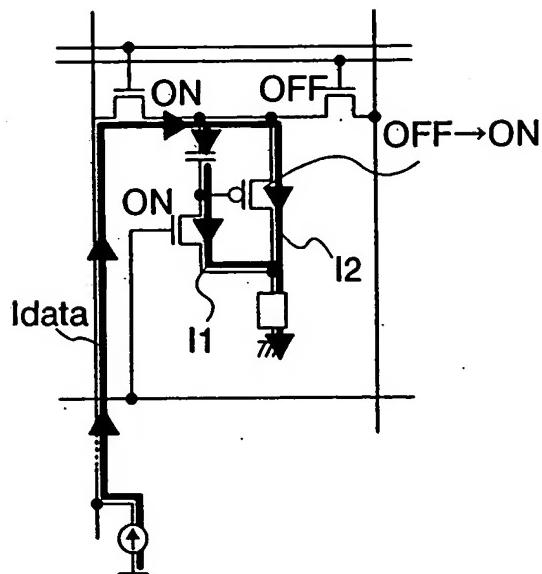


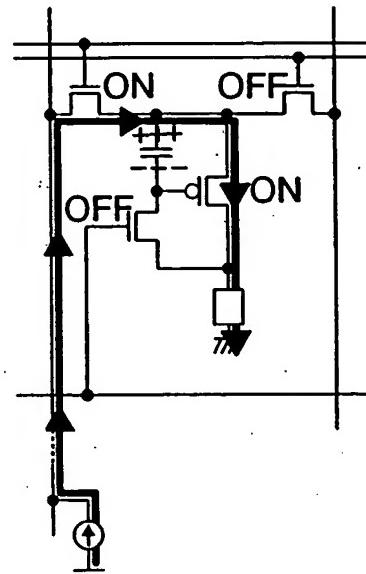
図 17

18/52

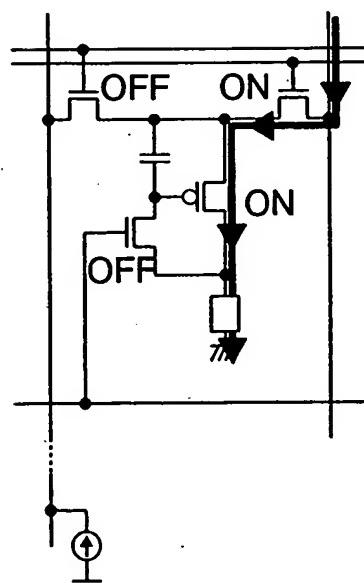
(A) 信号入力時



(B) 信号入力完了時



(C) 発光時



(D)

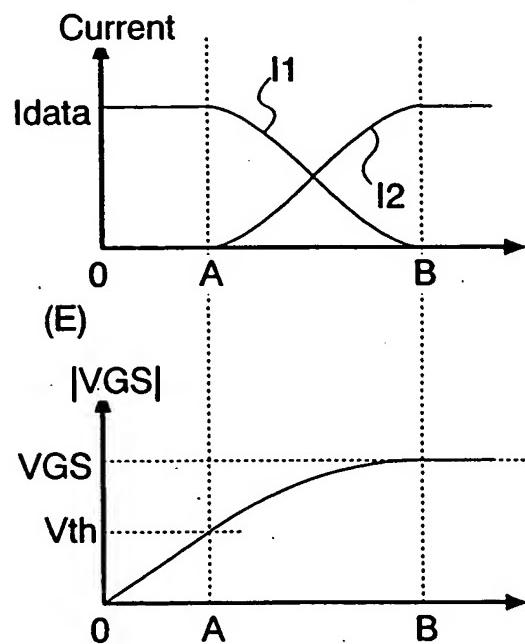


図18

19/52

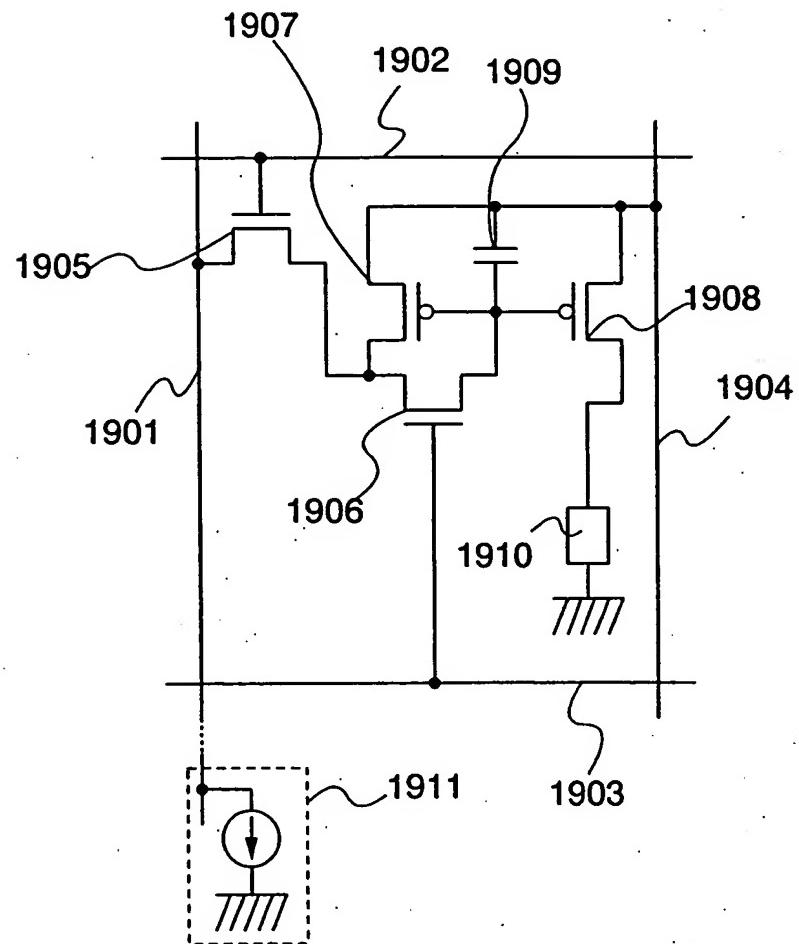
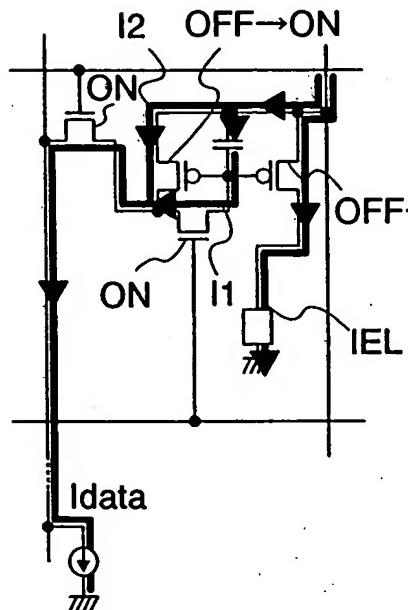


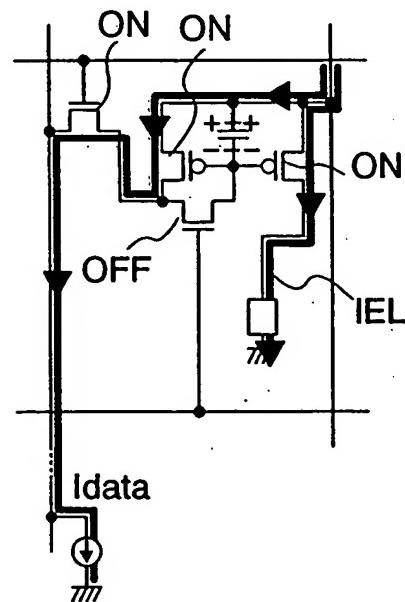
図 19

20/52

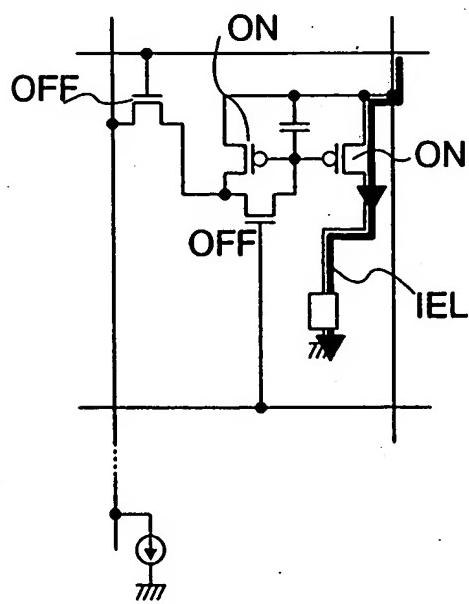
(A) 信号入力時



(B) 信号入力完了時



(C) 発光時



(D)

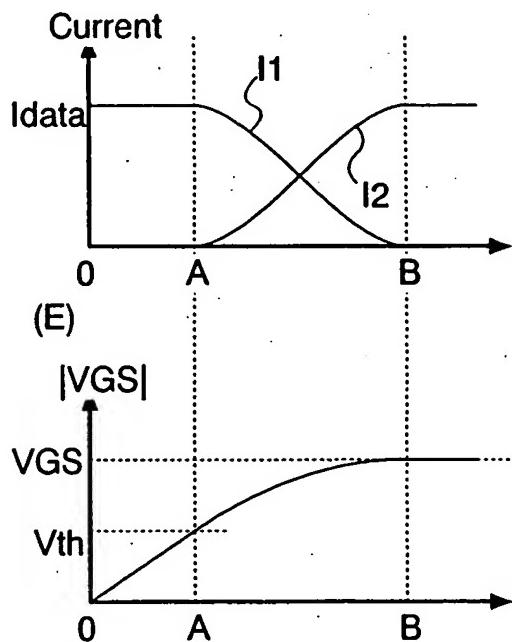


図20

21/52

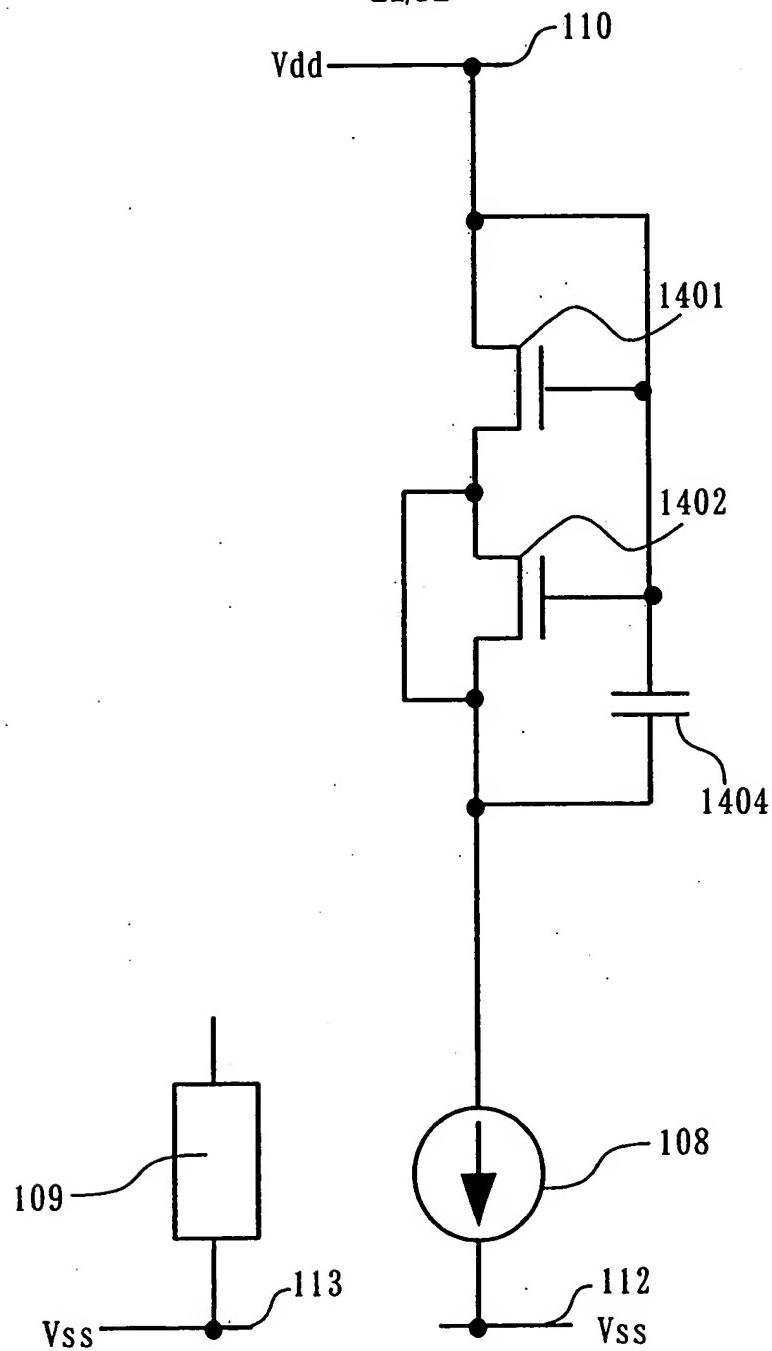


図21

22/52

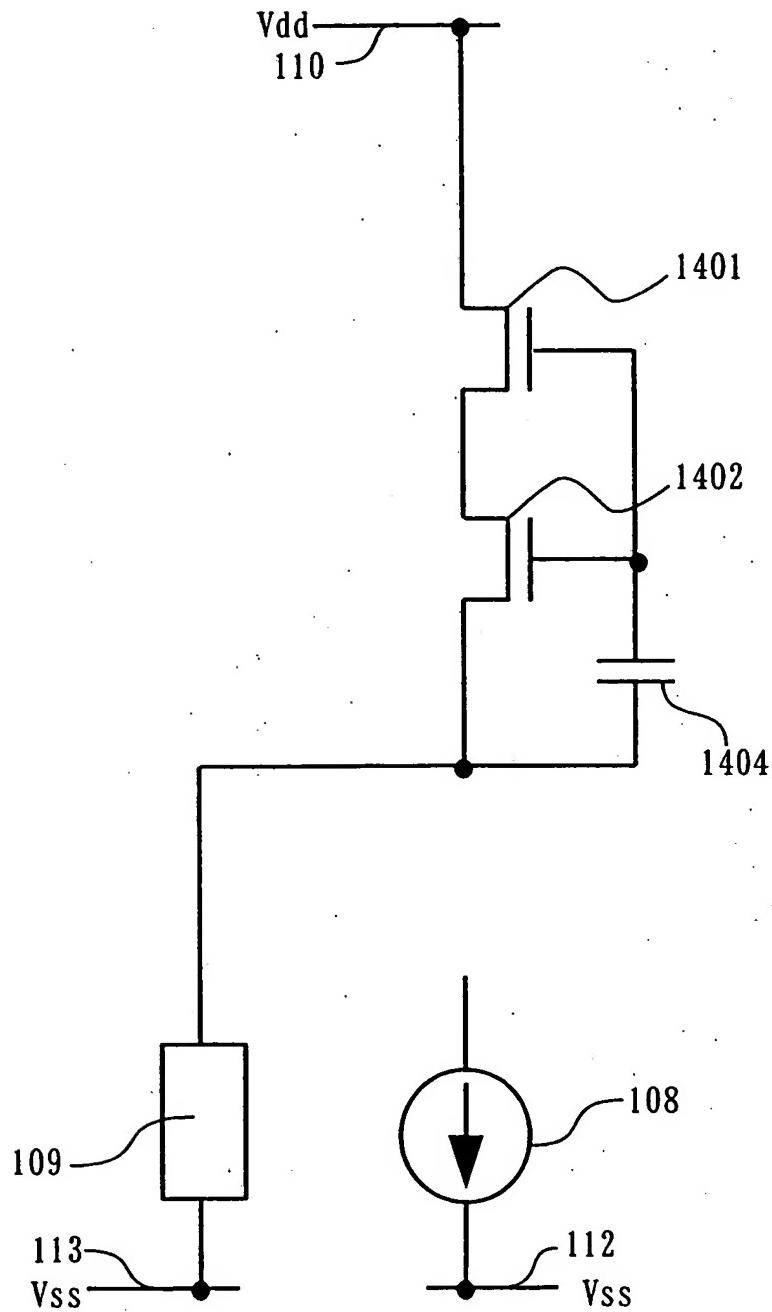


図22

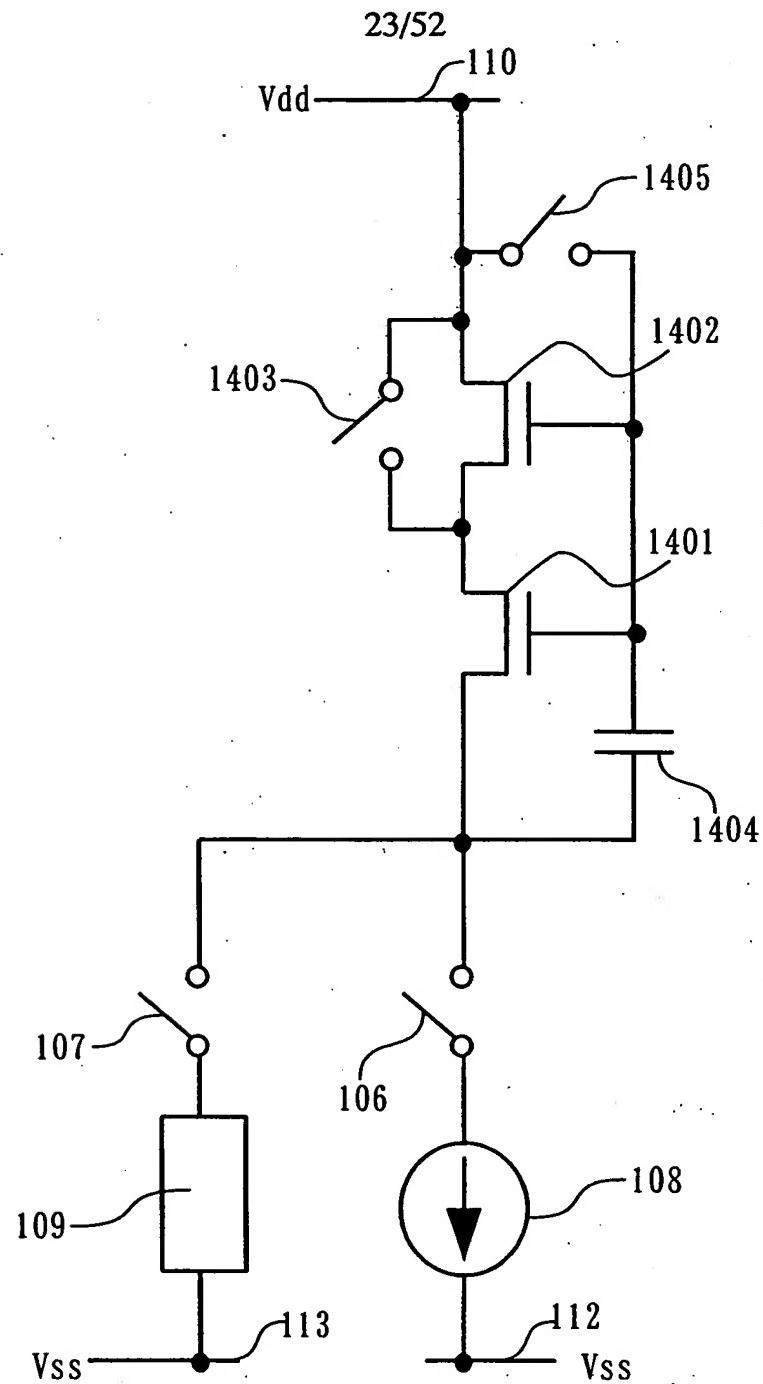


図23

24/52

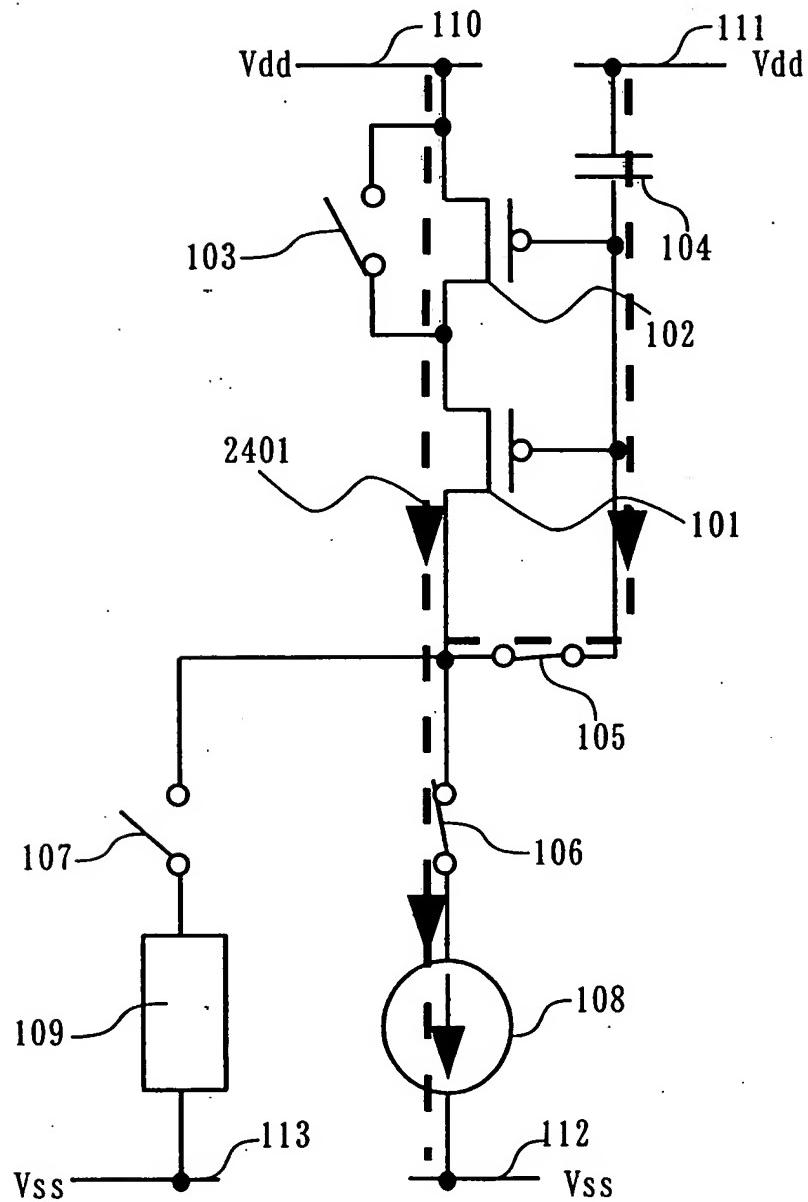


図24

25/52

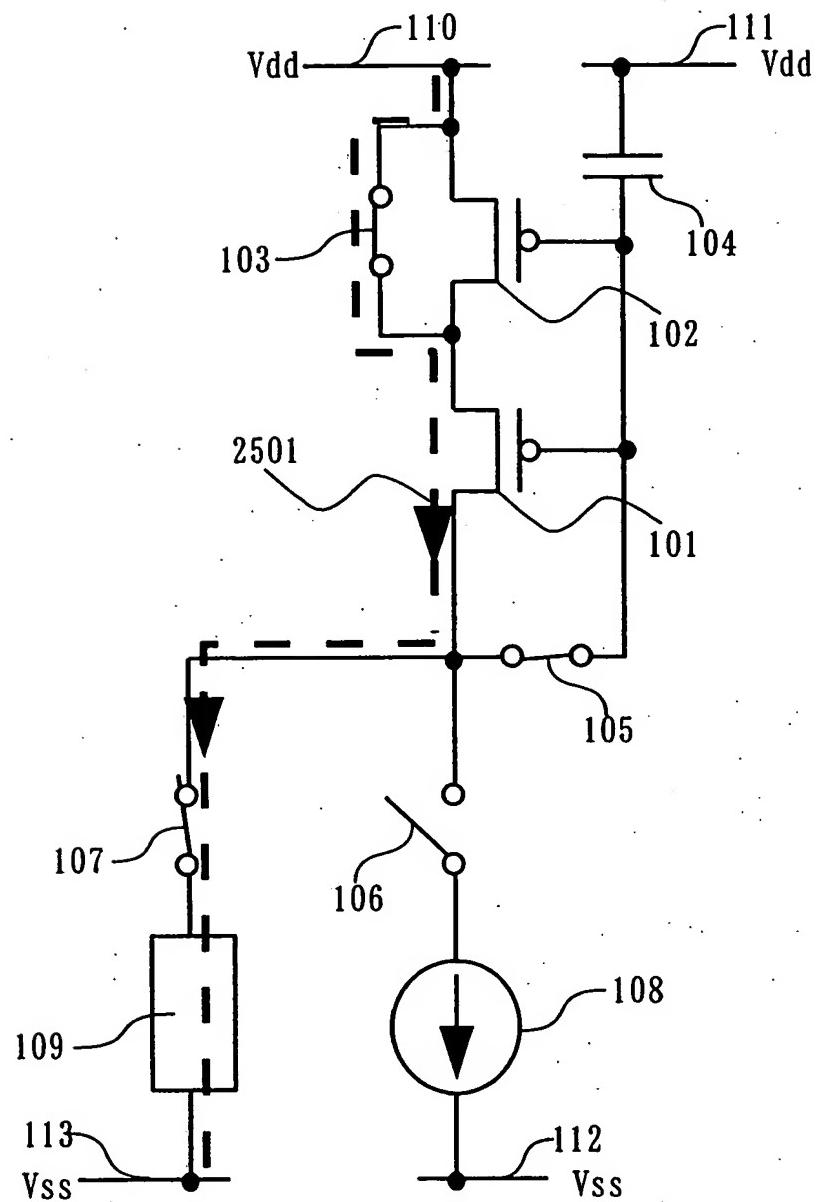


図25

26/52

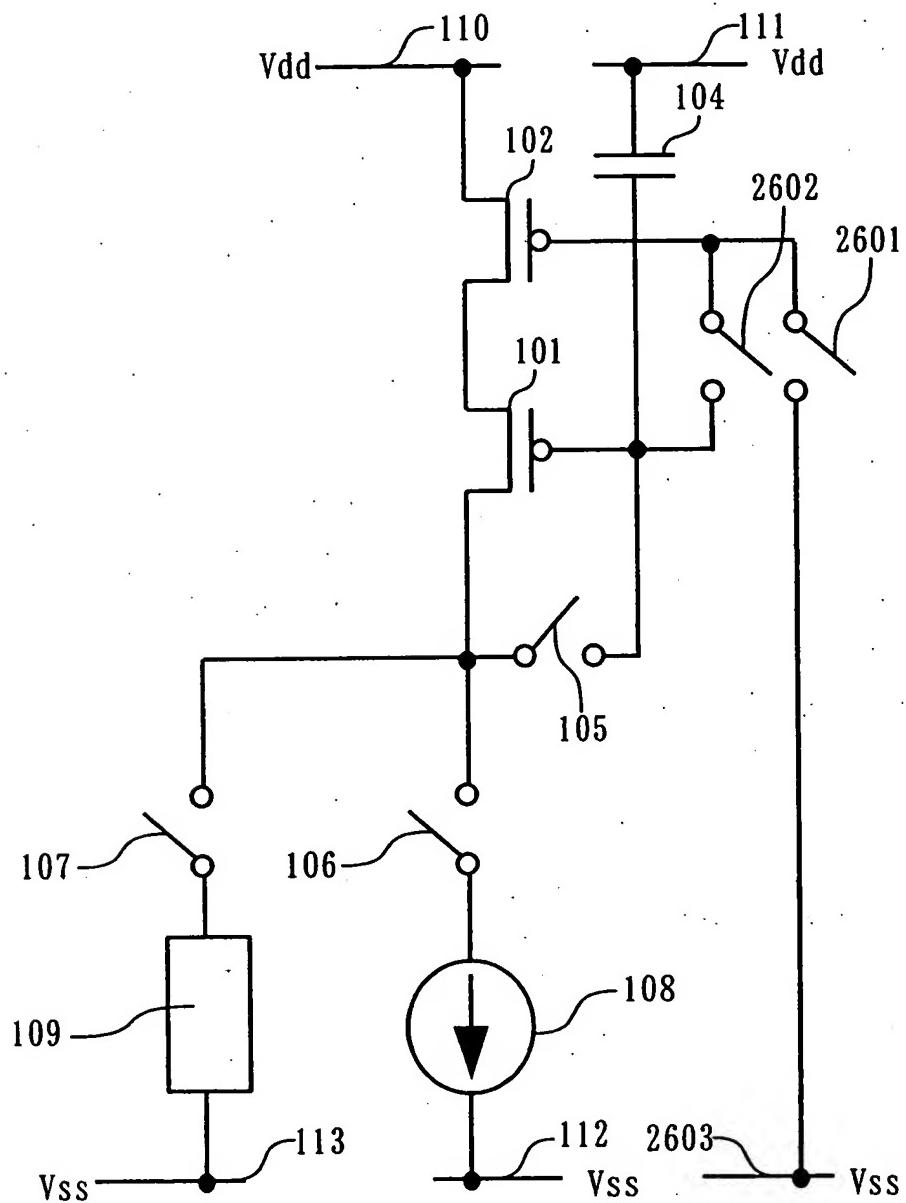


図26

27/52

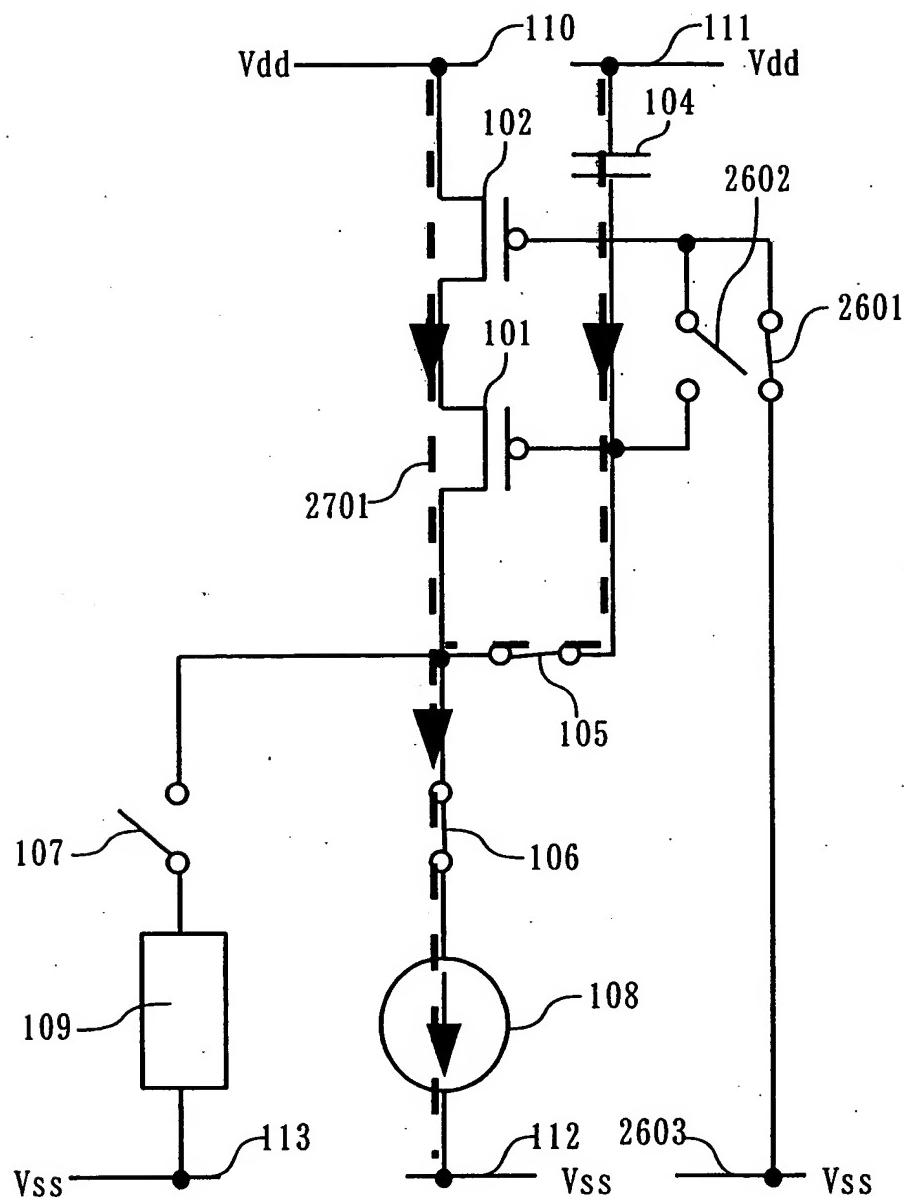


図27

28/52

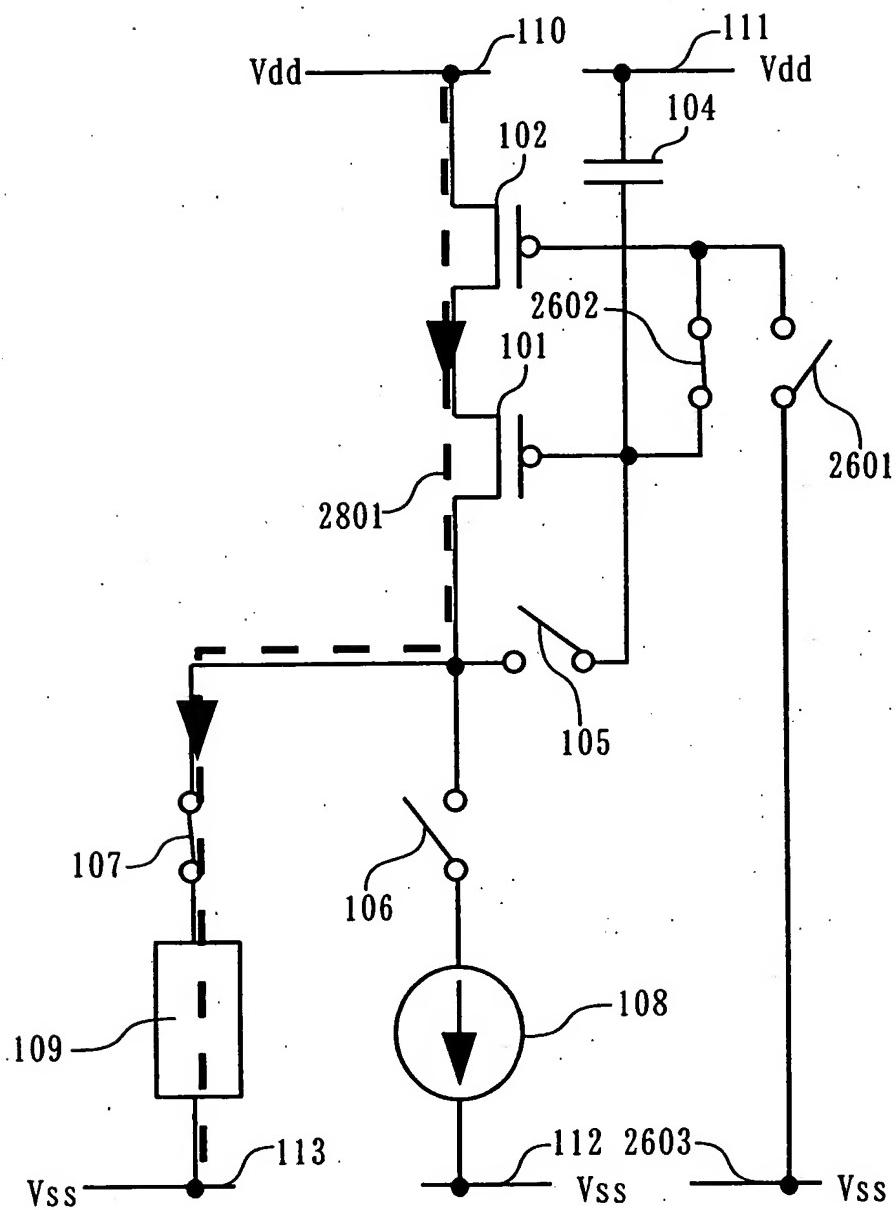


図28

29/52

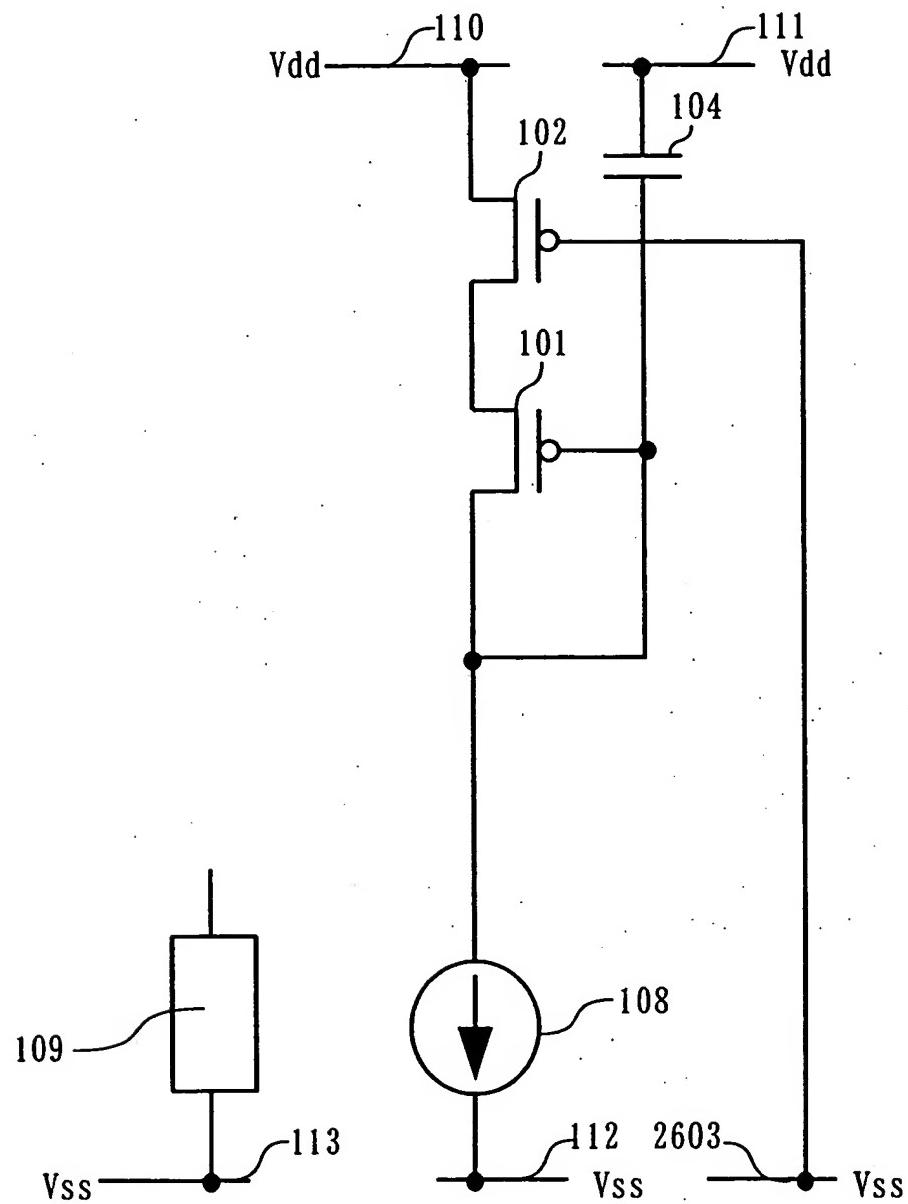


図29

30/52

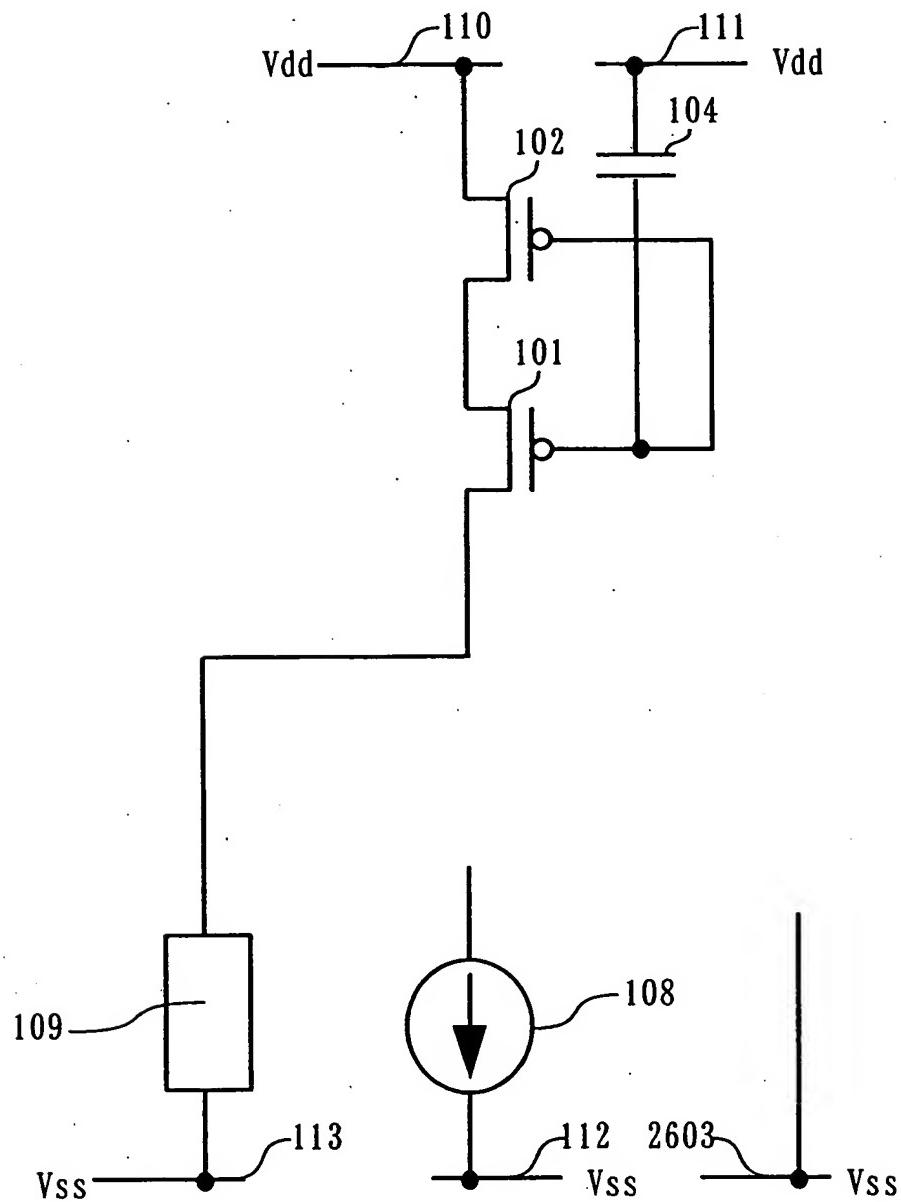


図30

31/52

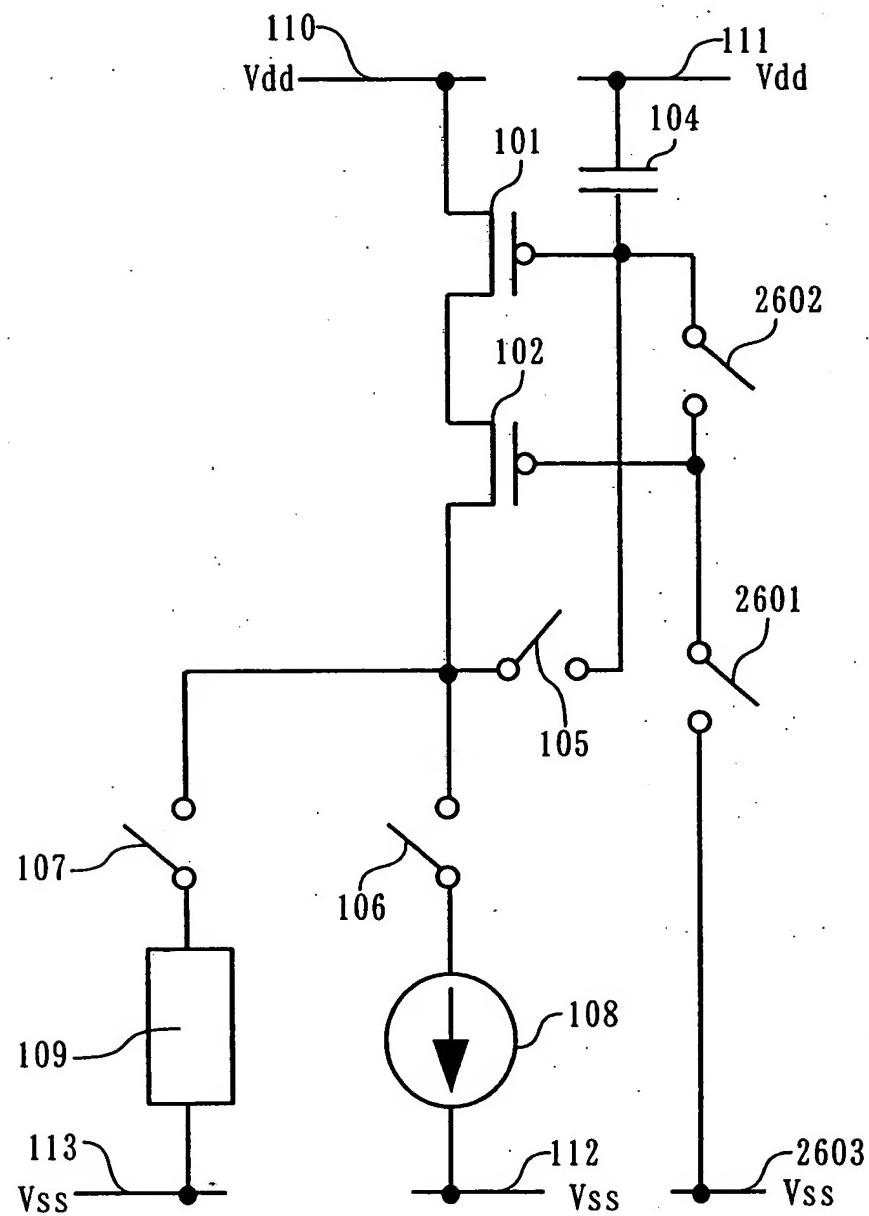


図31

32/52

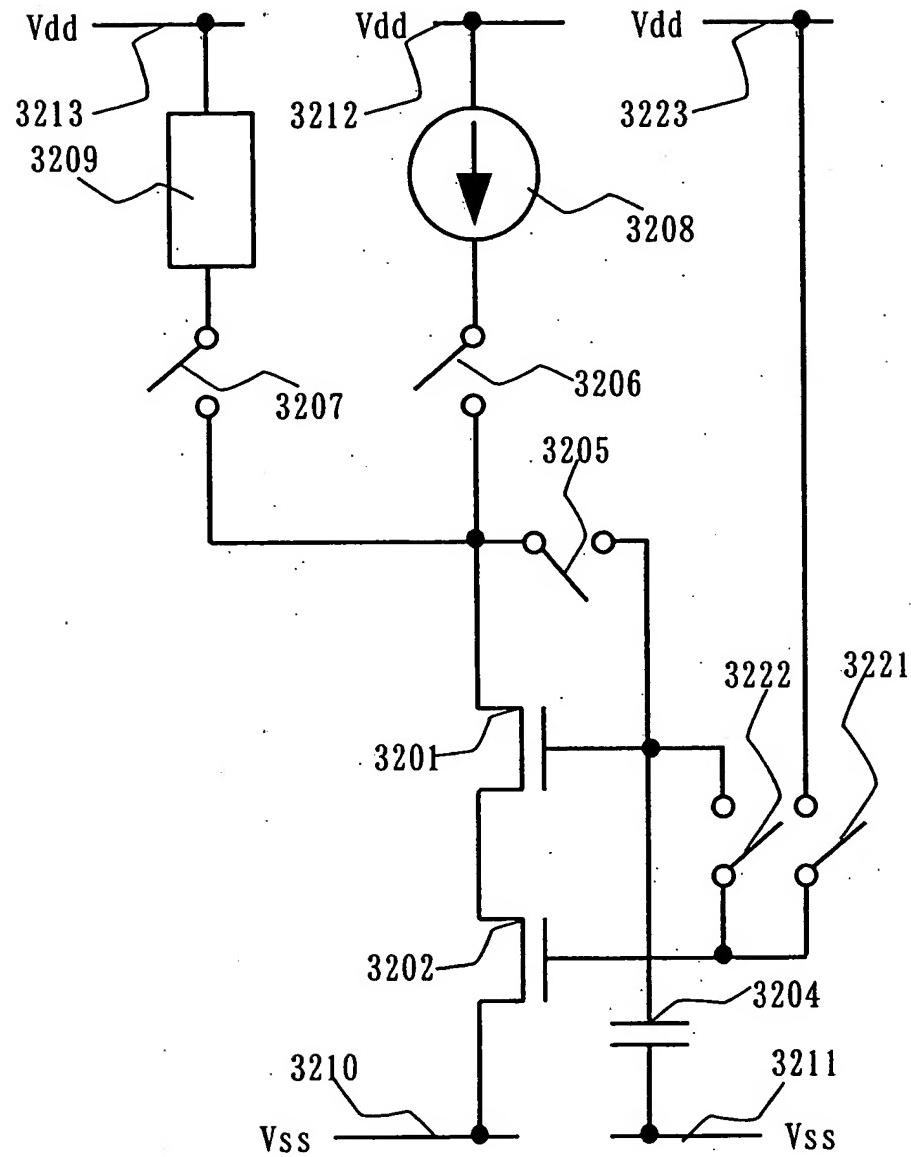


図32

33/52

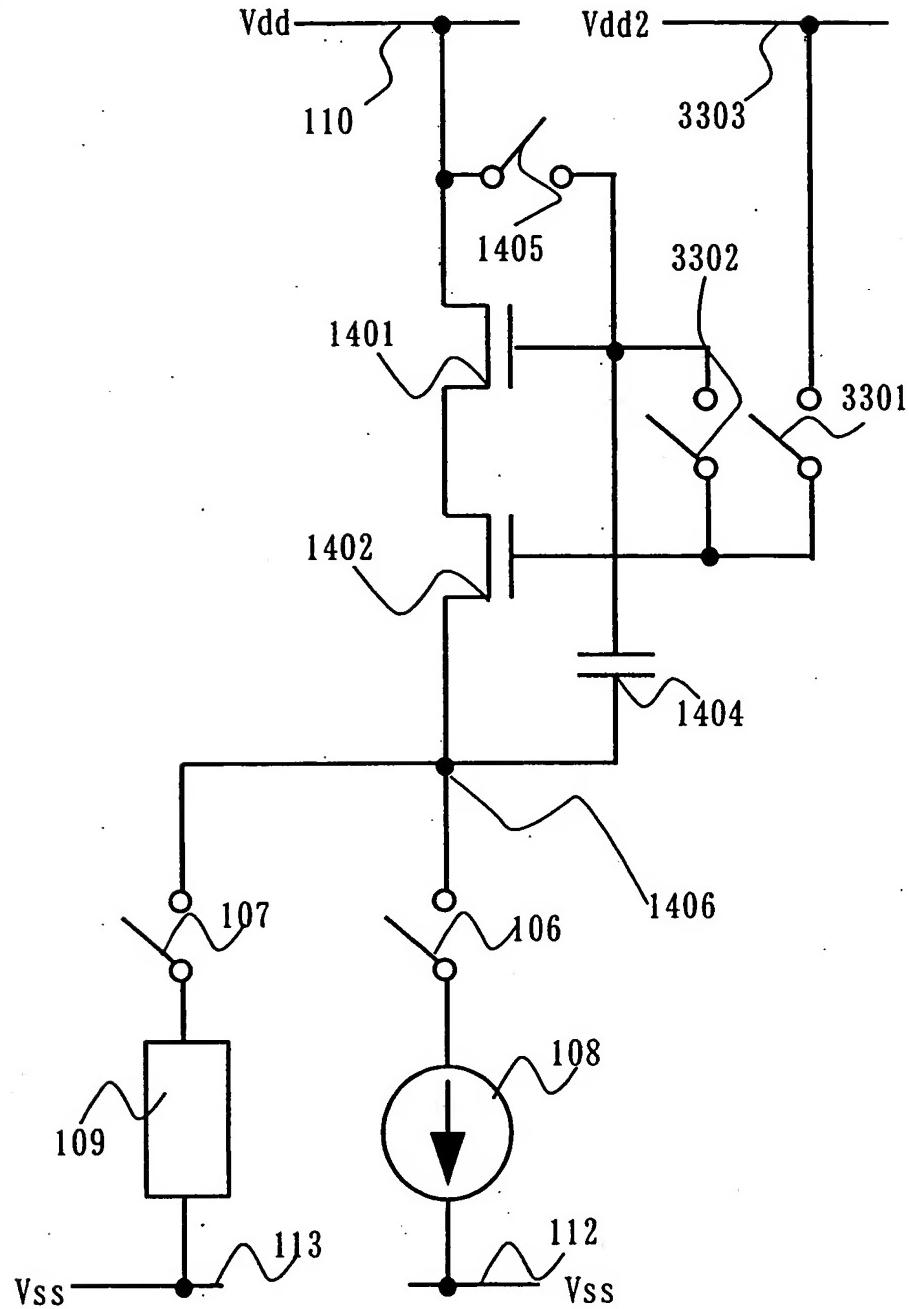


図33

34/52

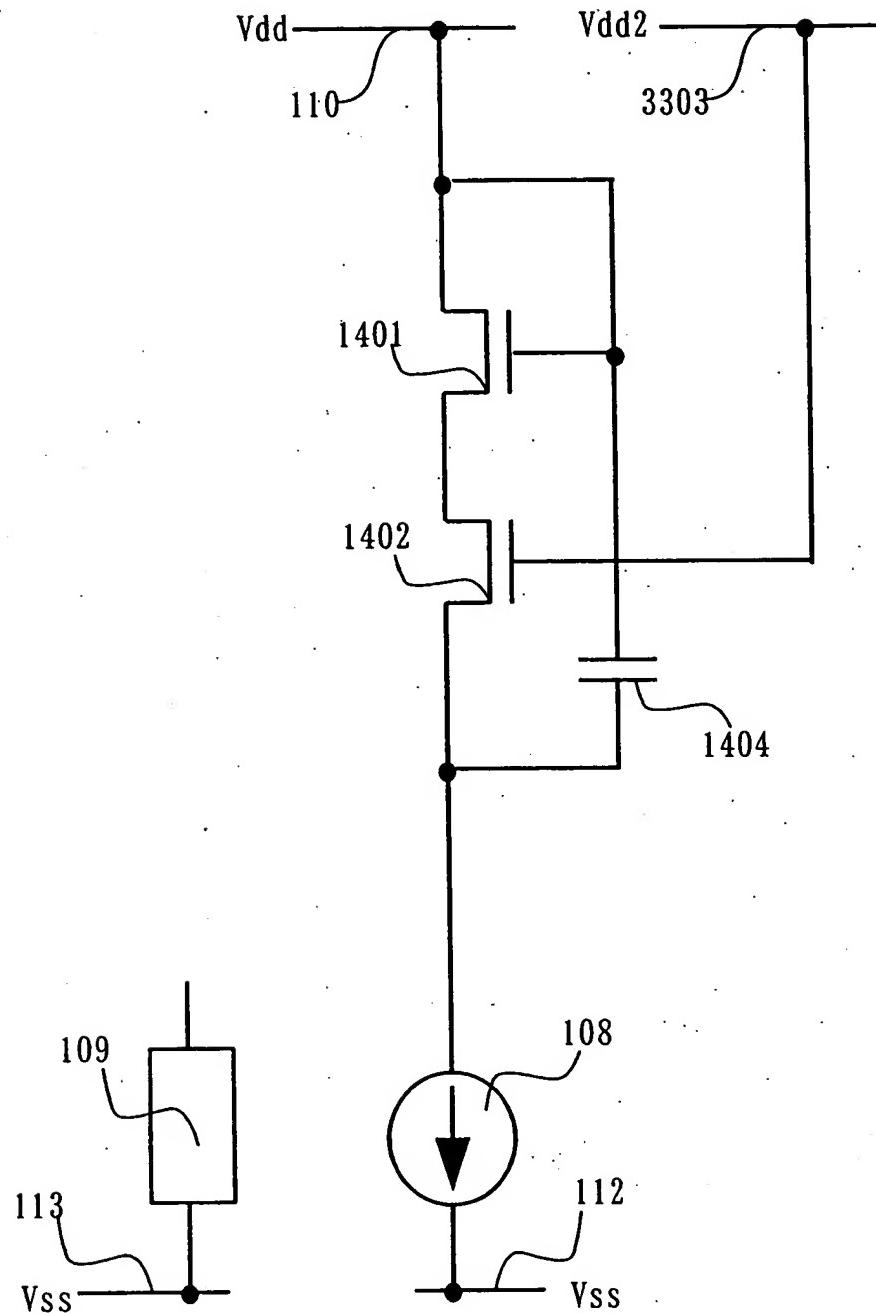


図34

35/52

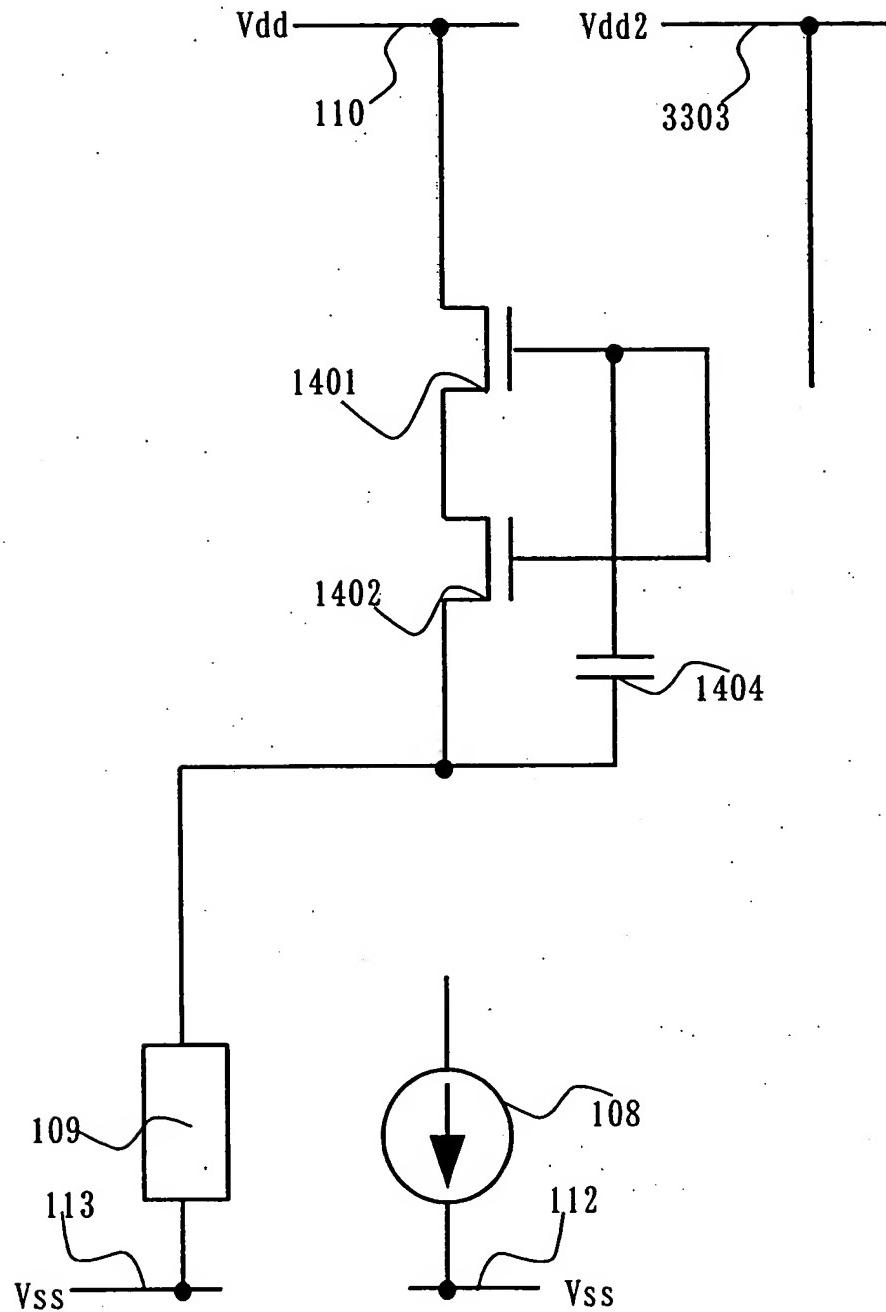


図35

36/52

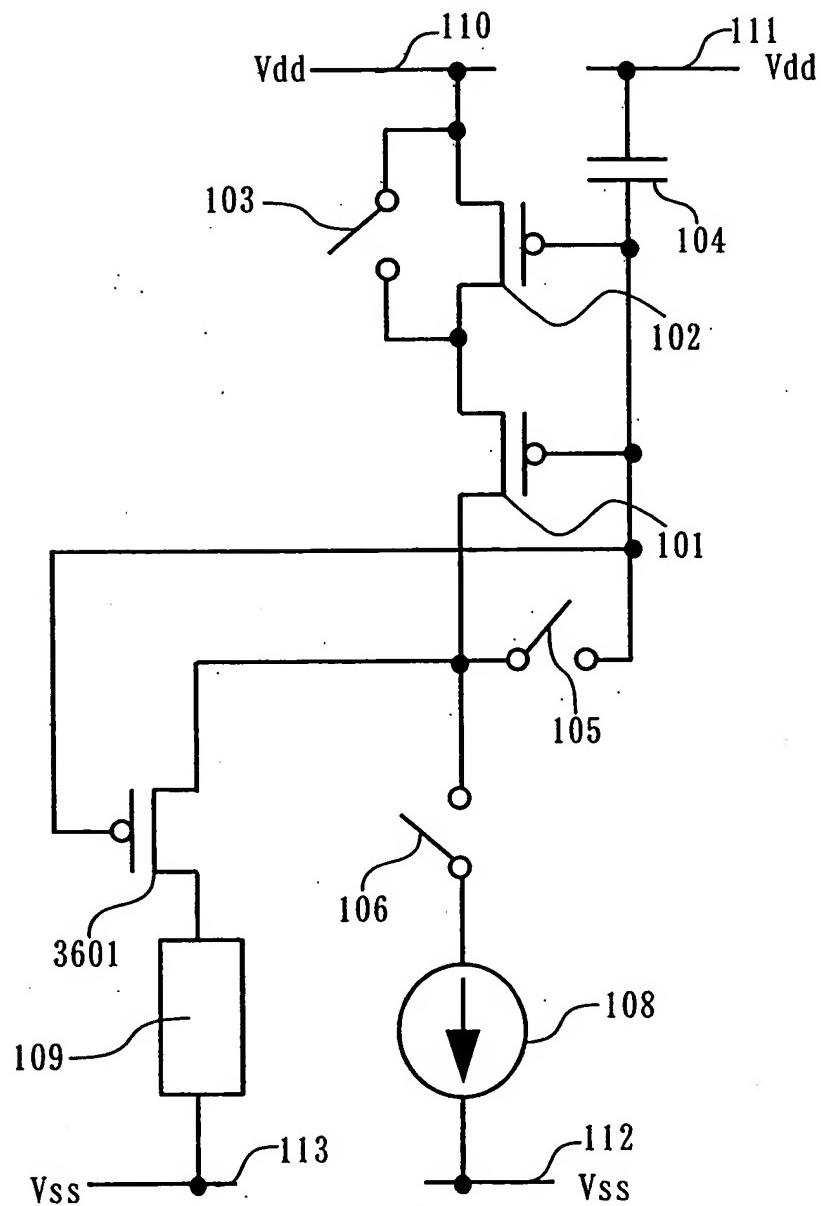


図36

37/52

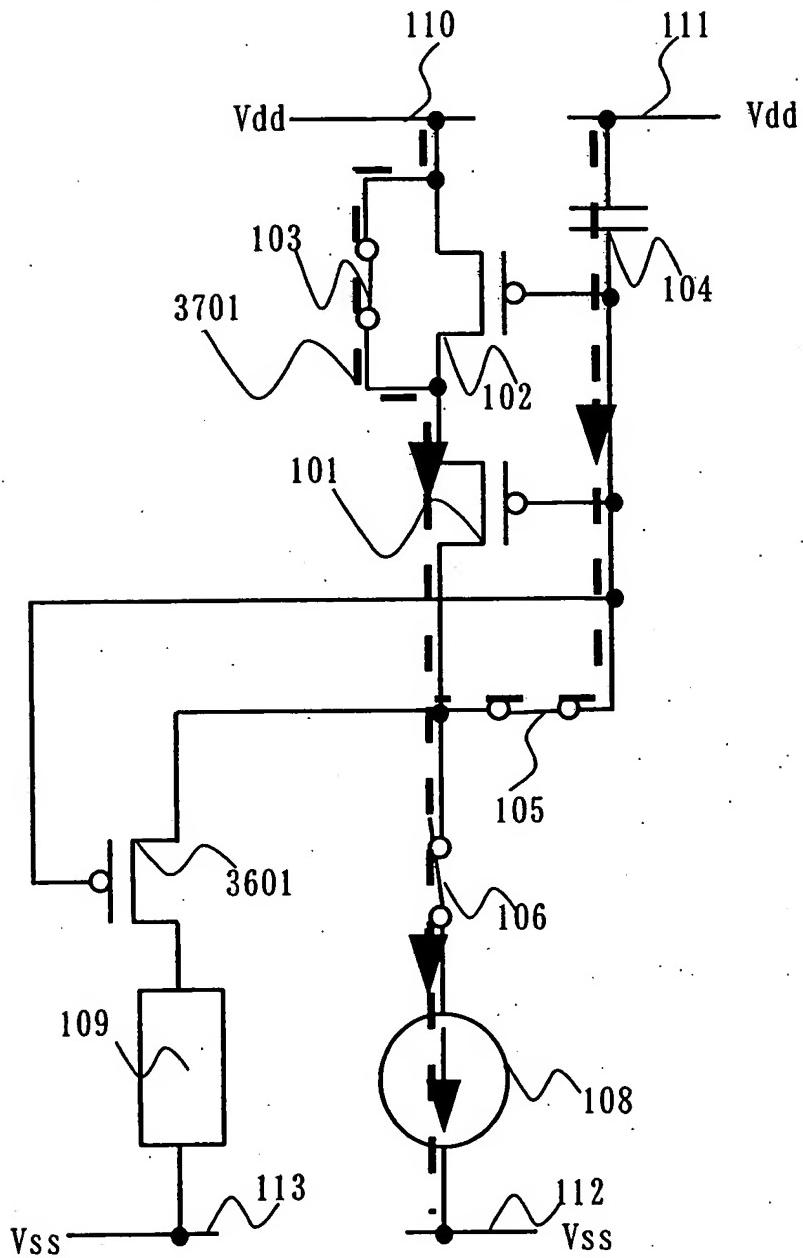


図37

38/52

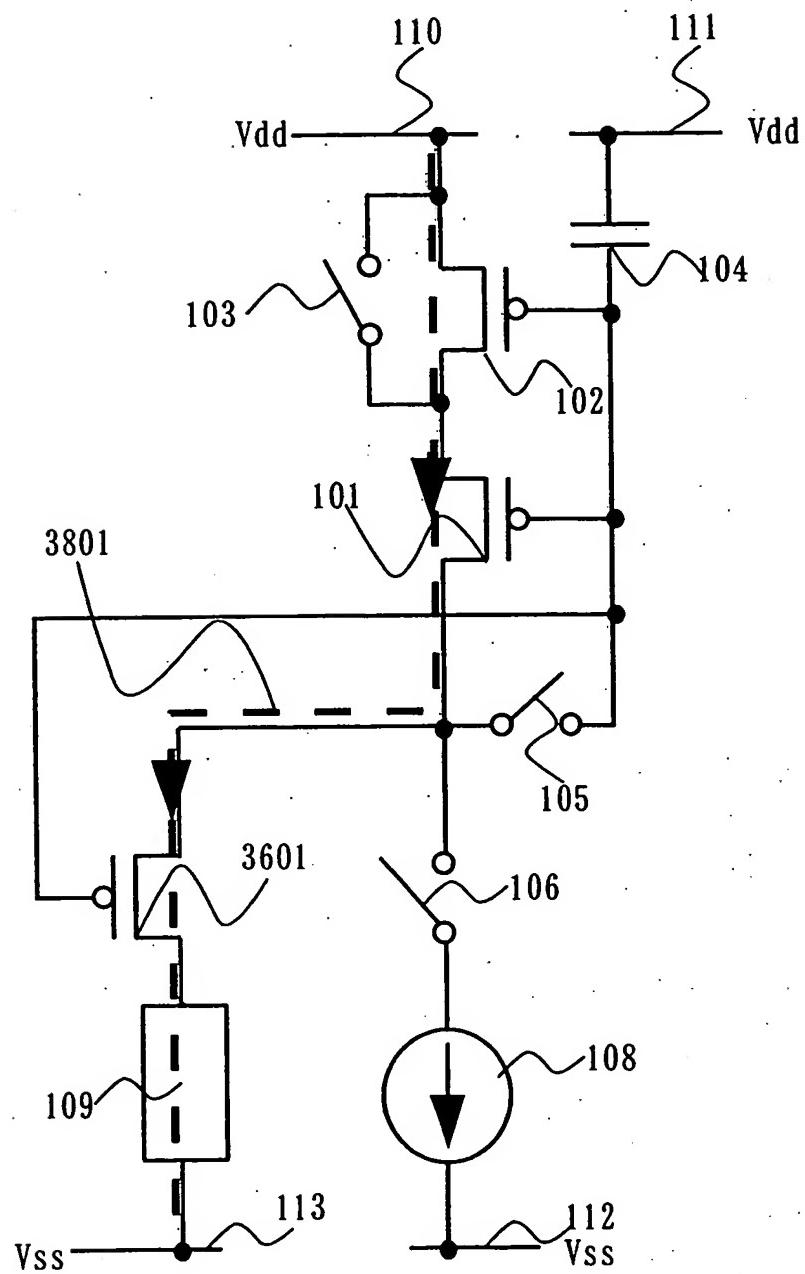


図38

39/52

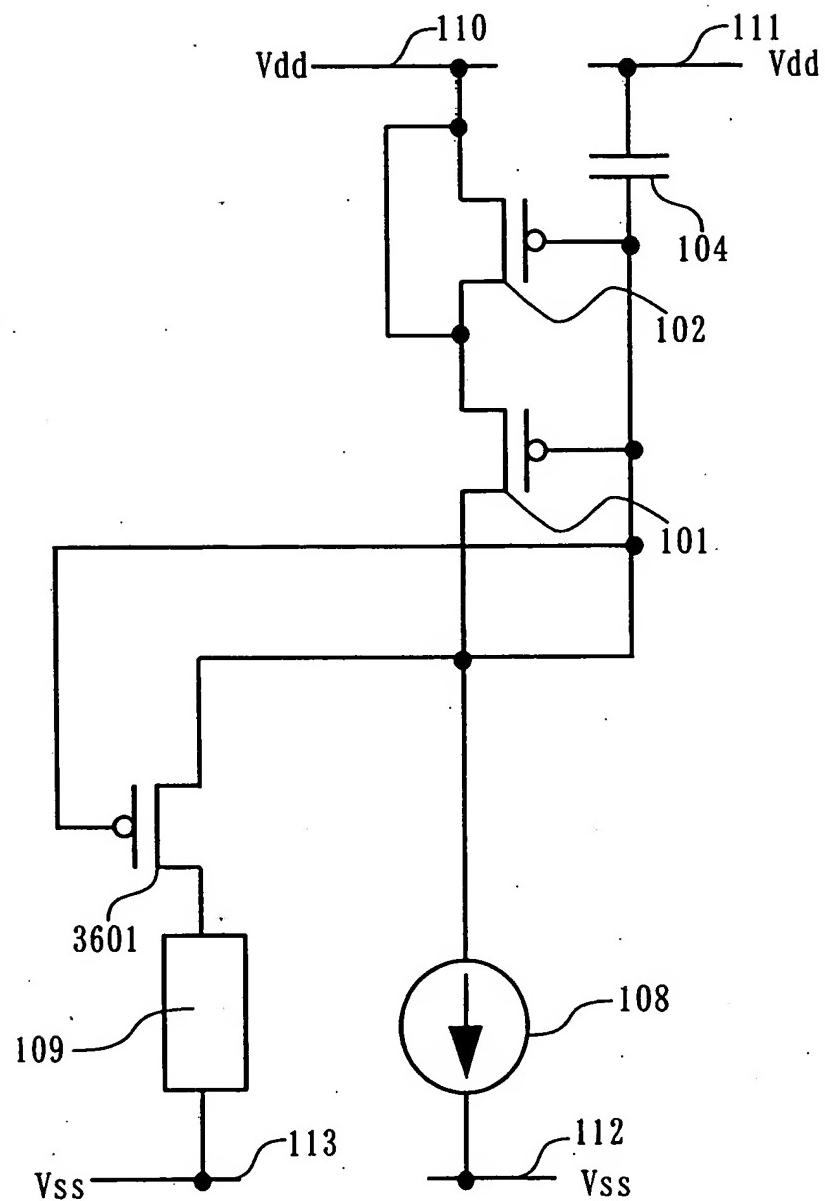


図39

40/52

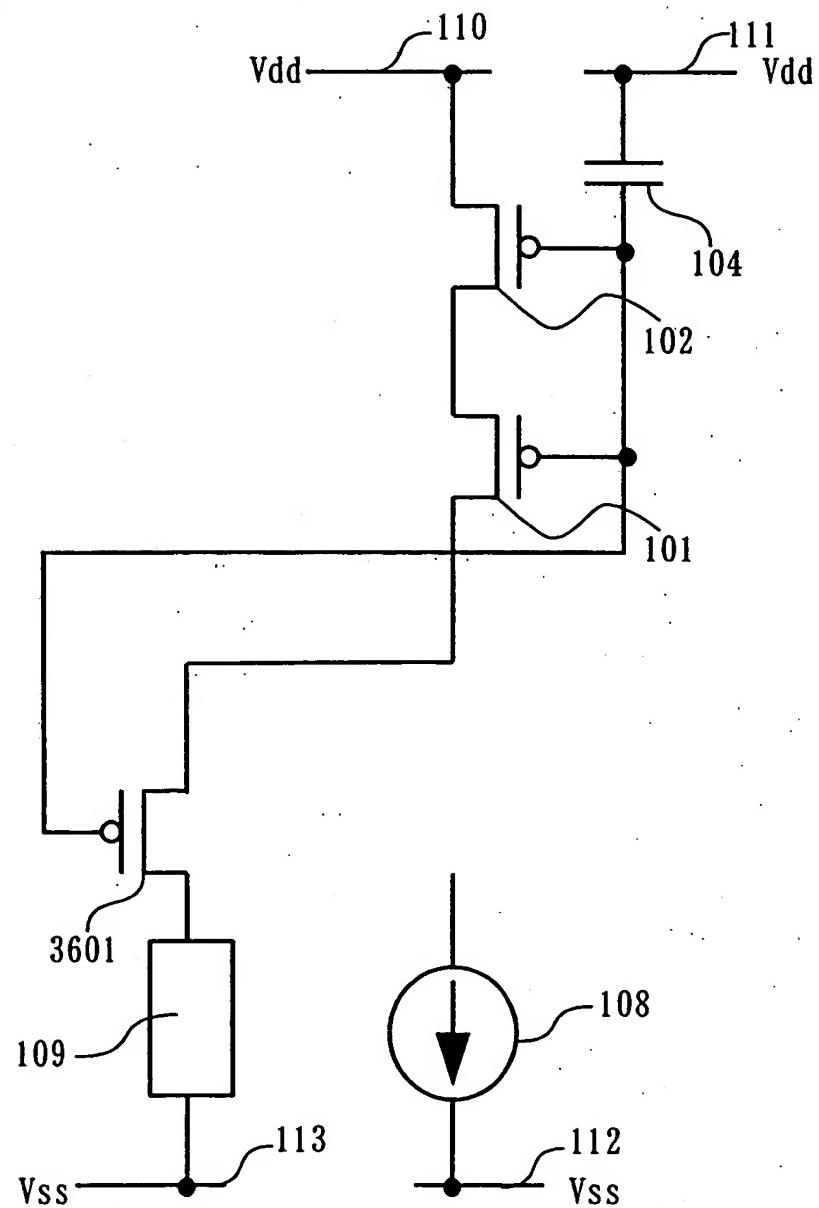


図40

41/52

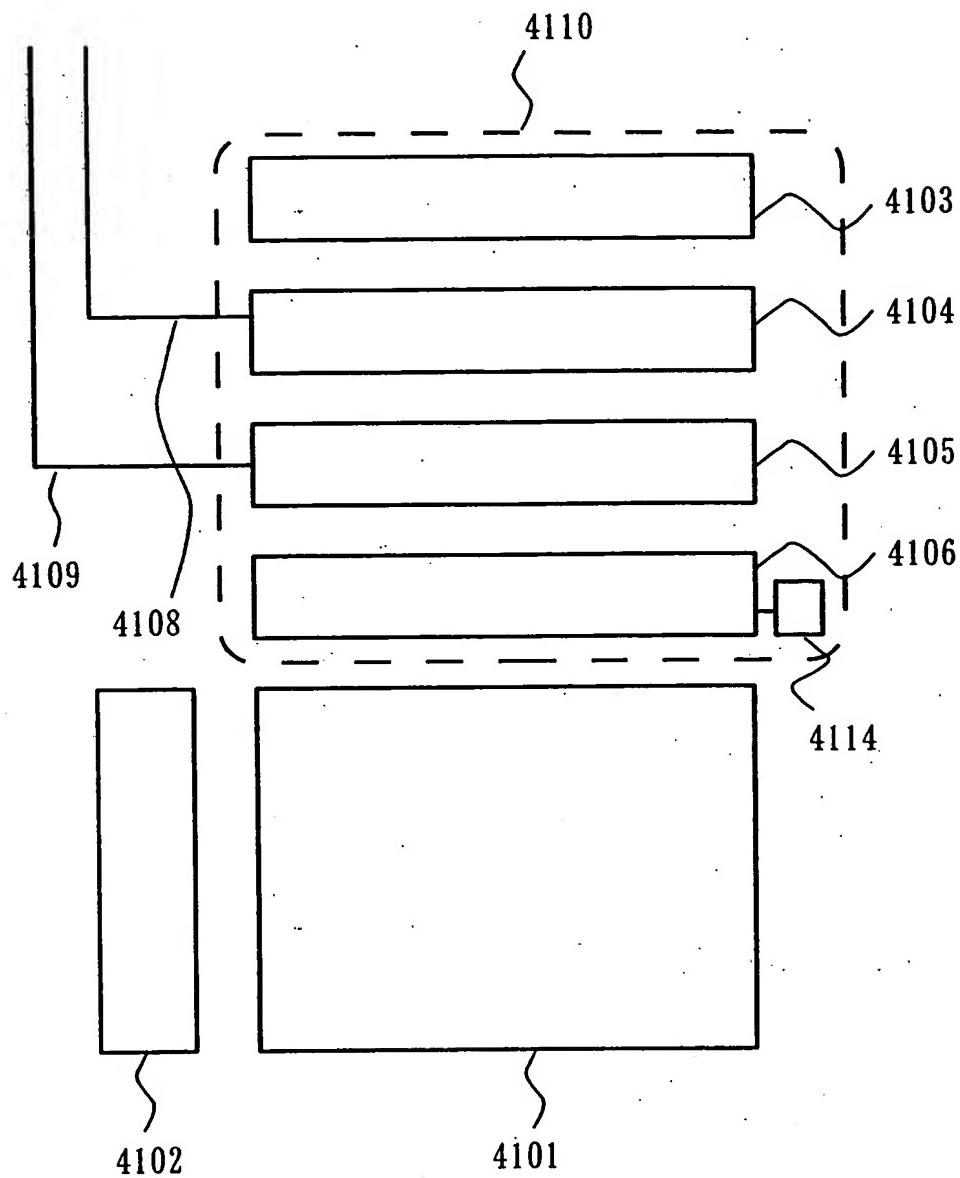


図4.1

42/52

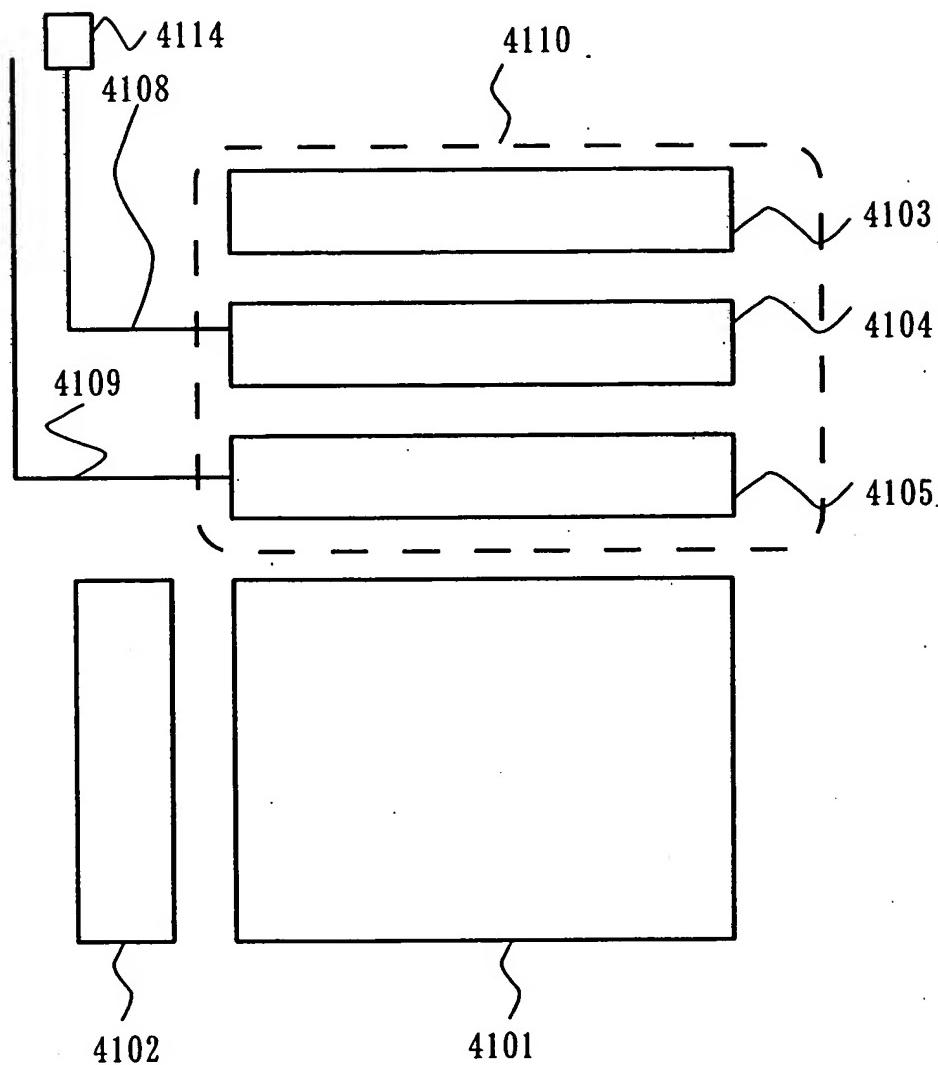
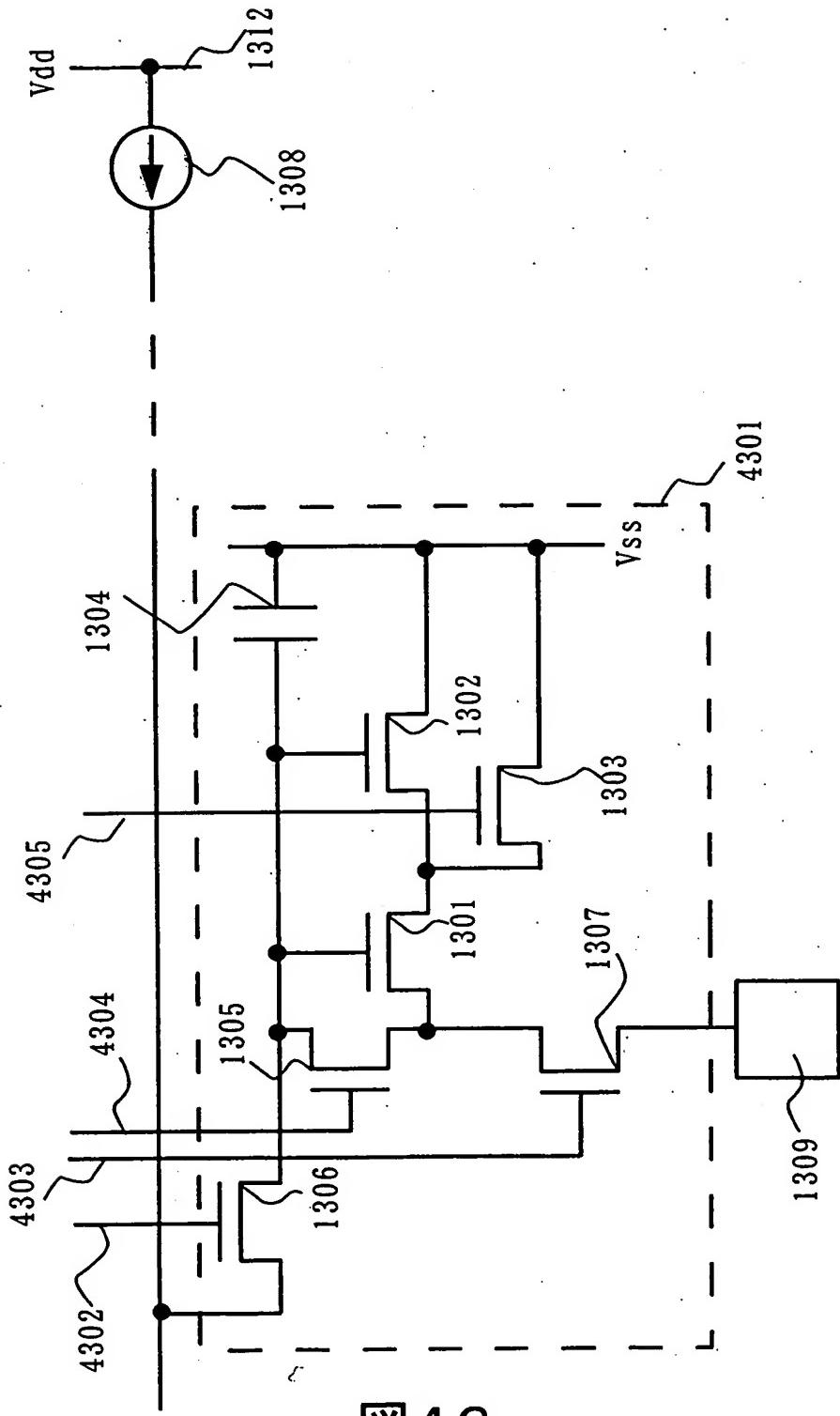


図 42

43/52



☒ 43

44/52

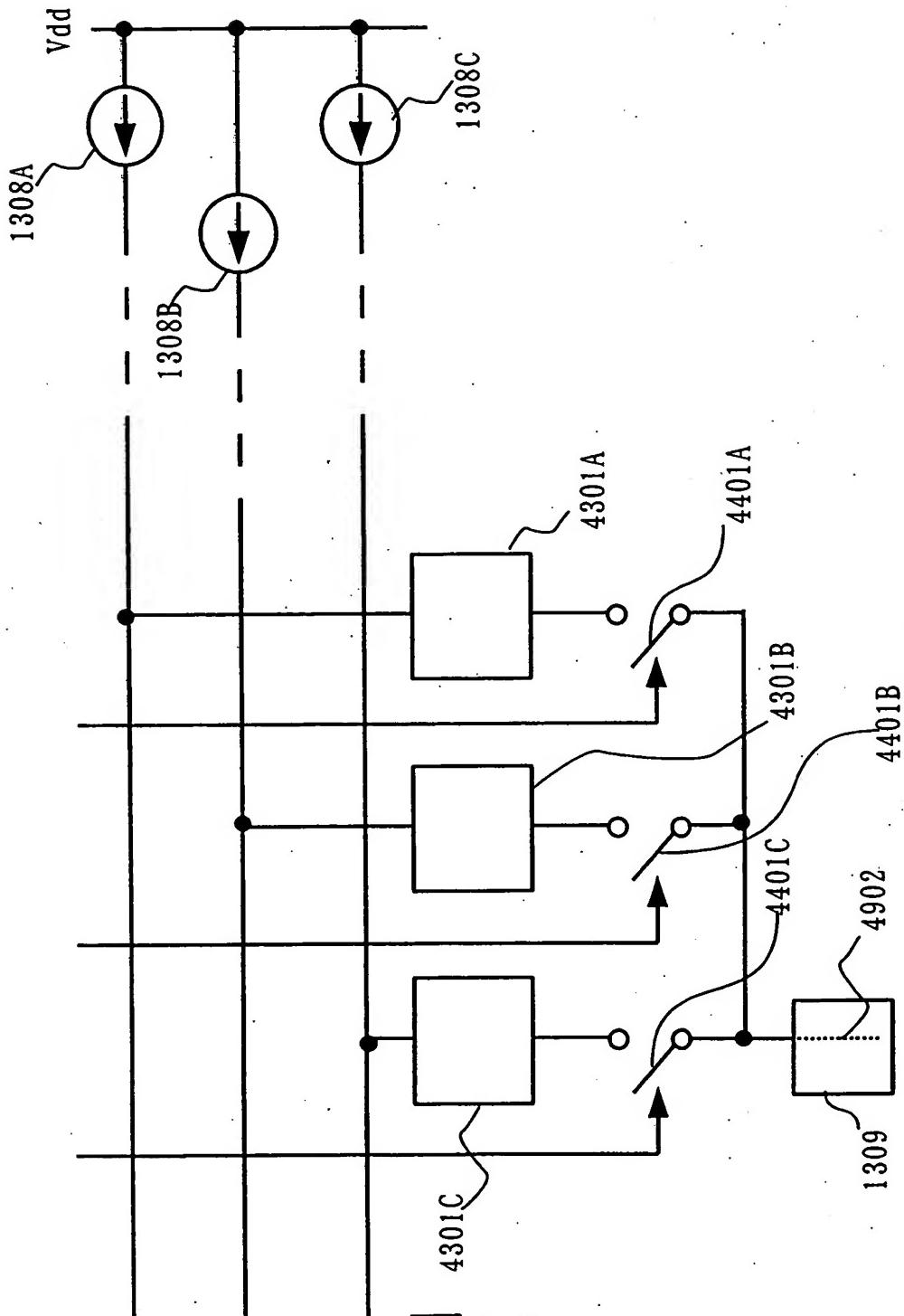


図44

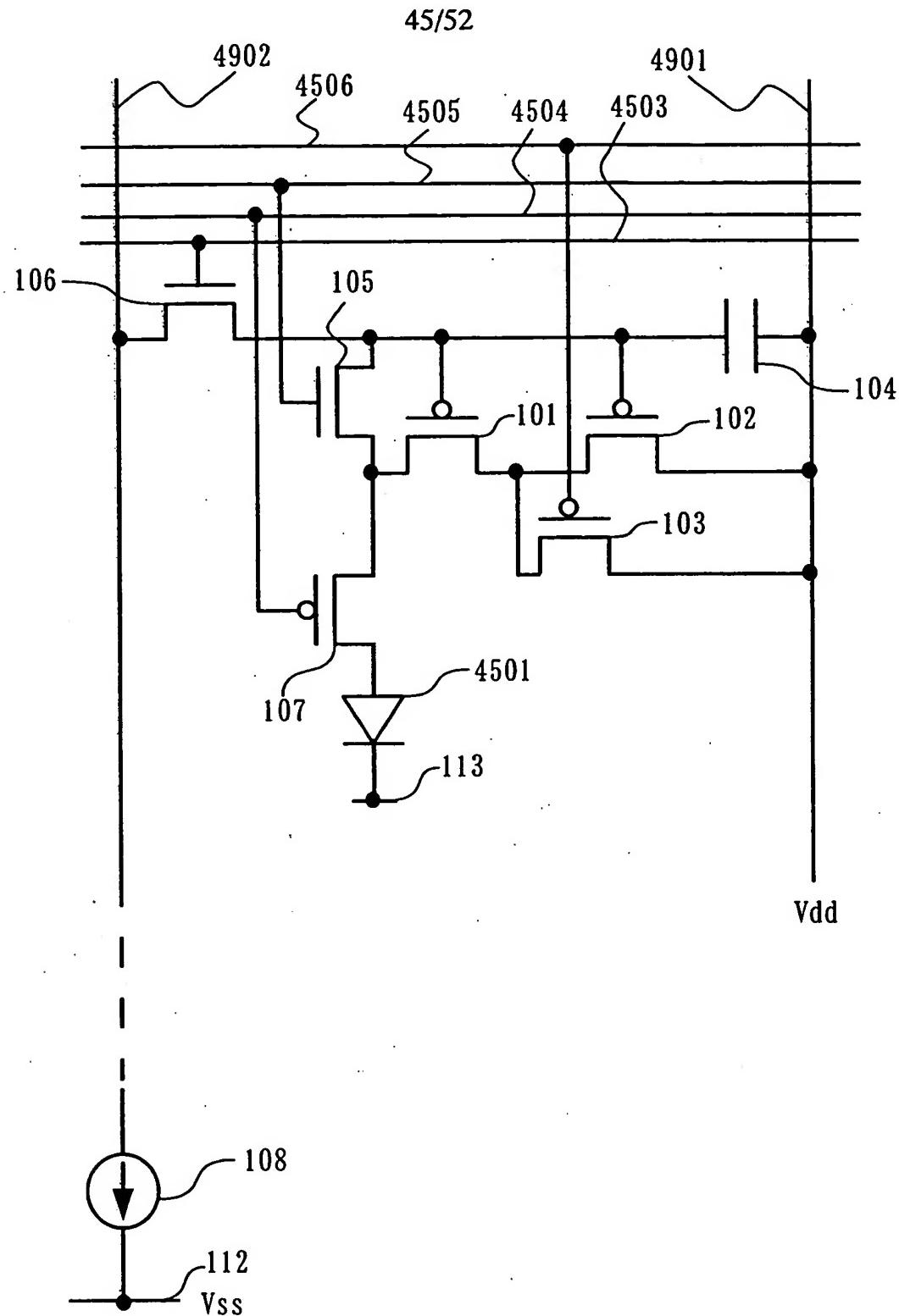


図45

46/52

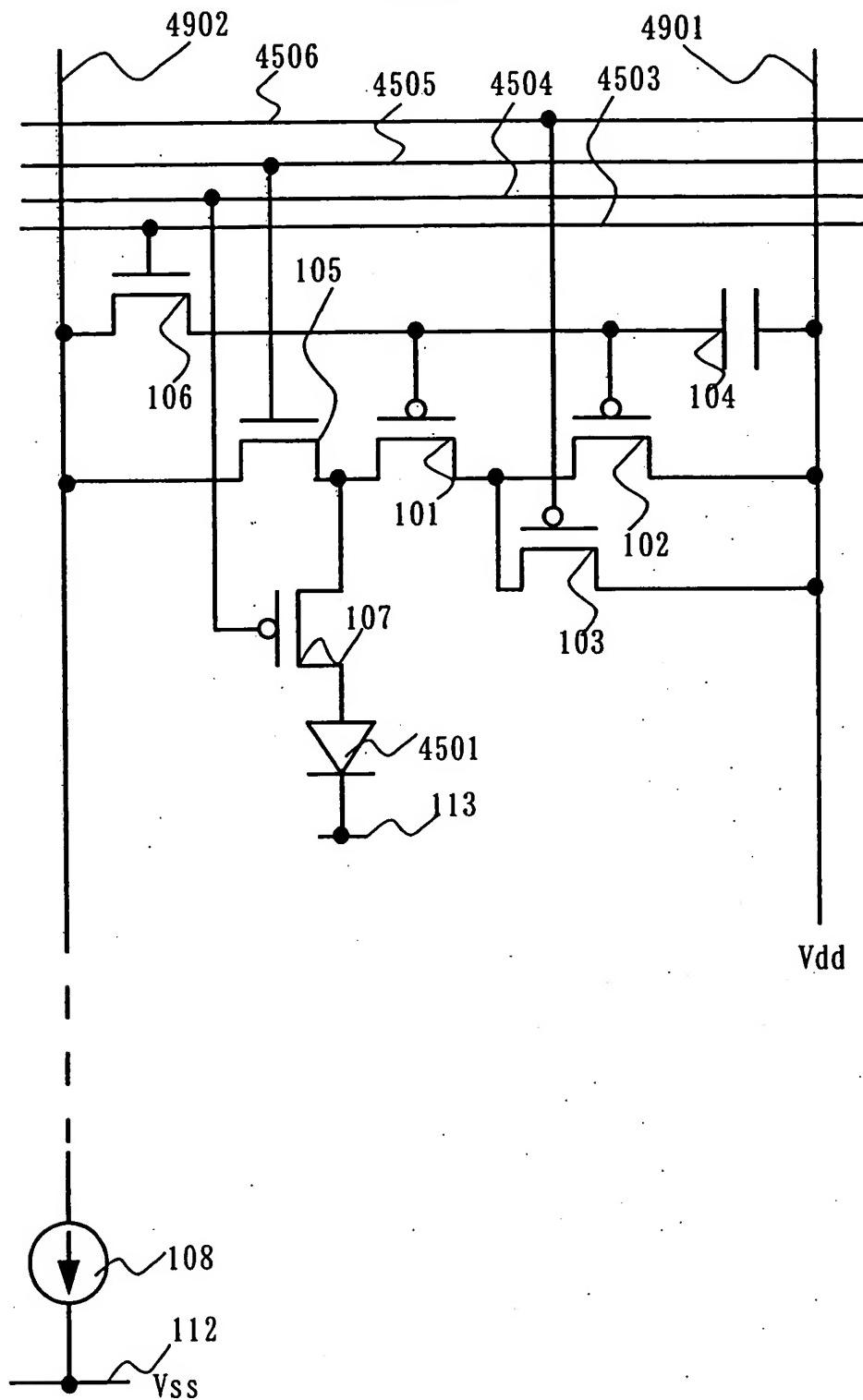


図46

47/52

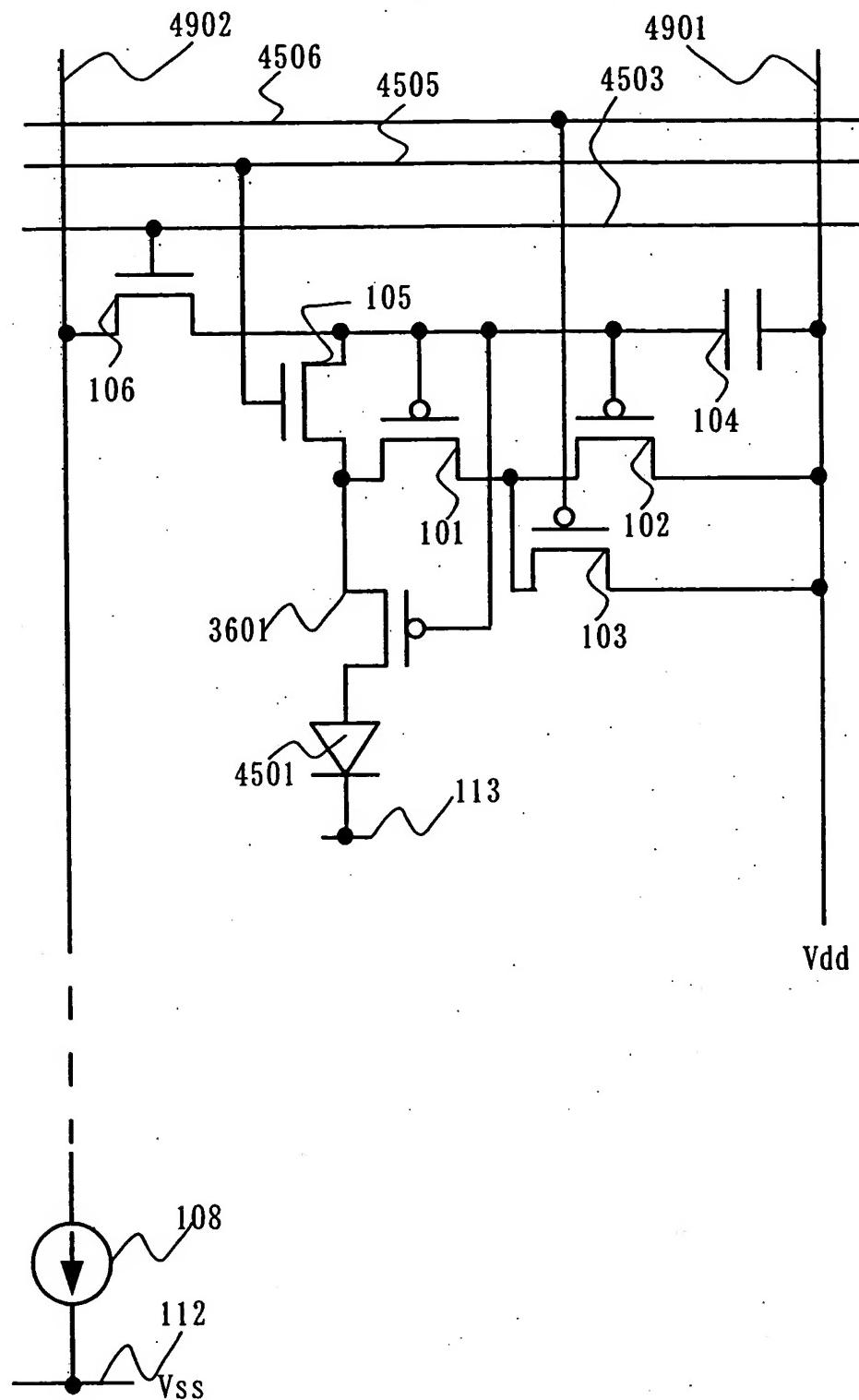


図47

48/52

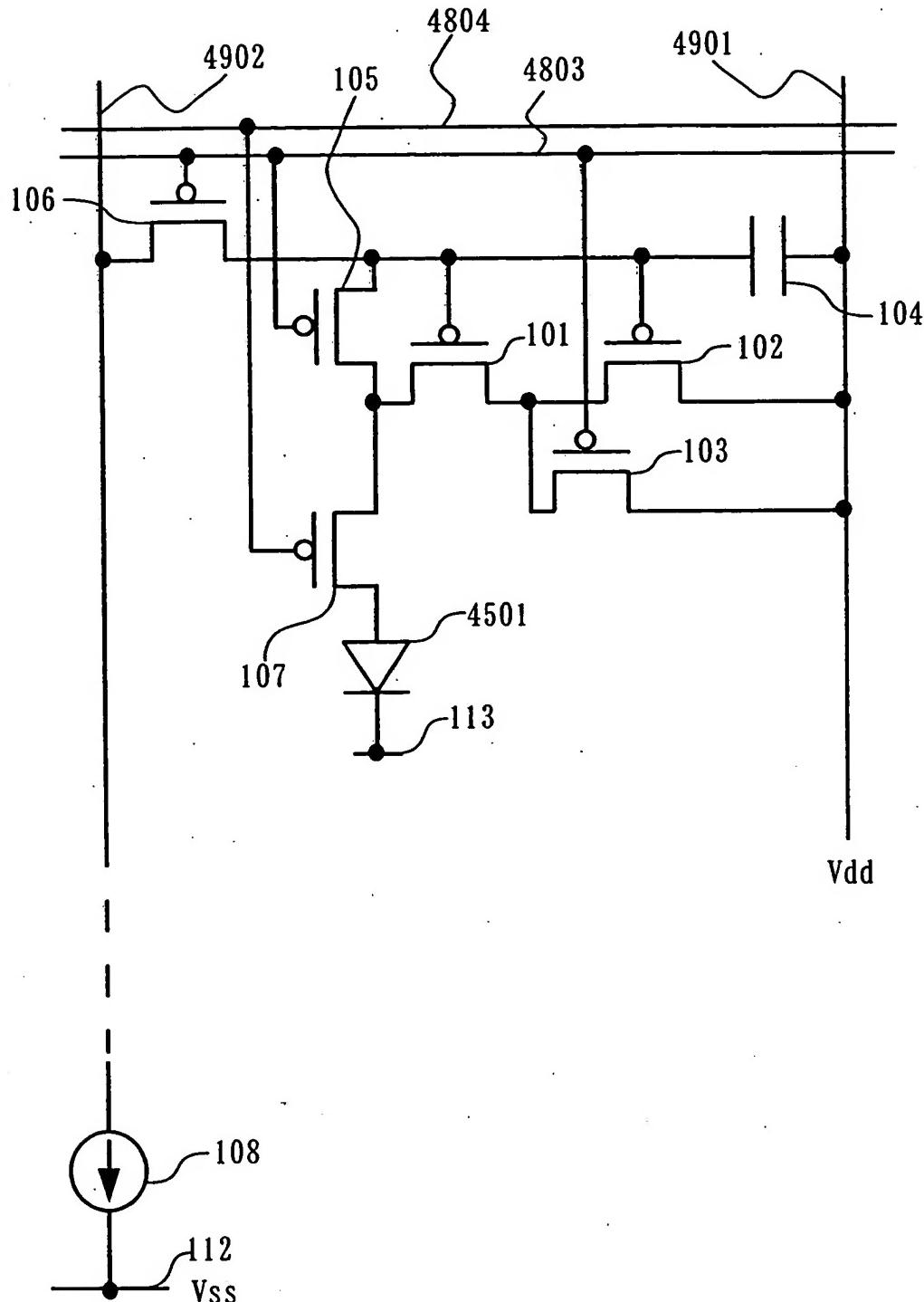


図48

49/52

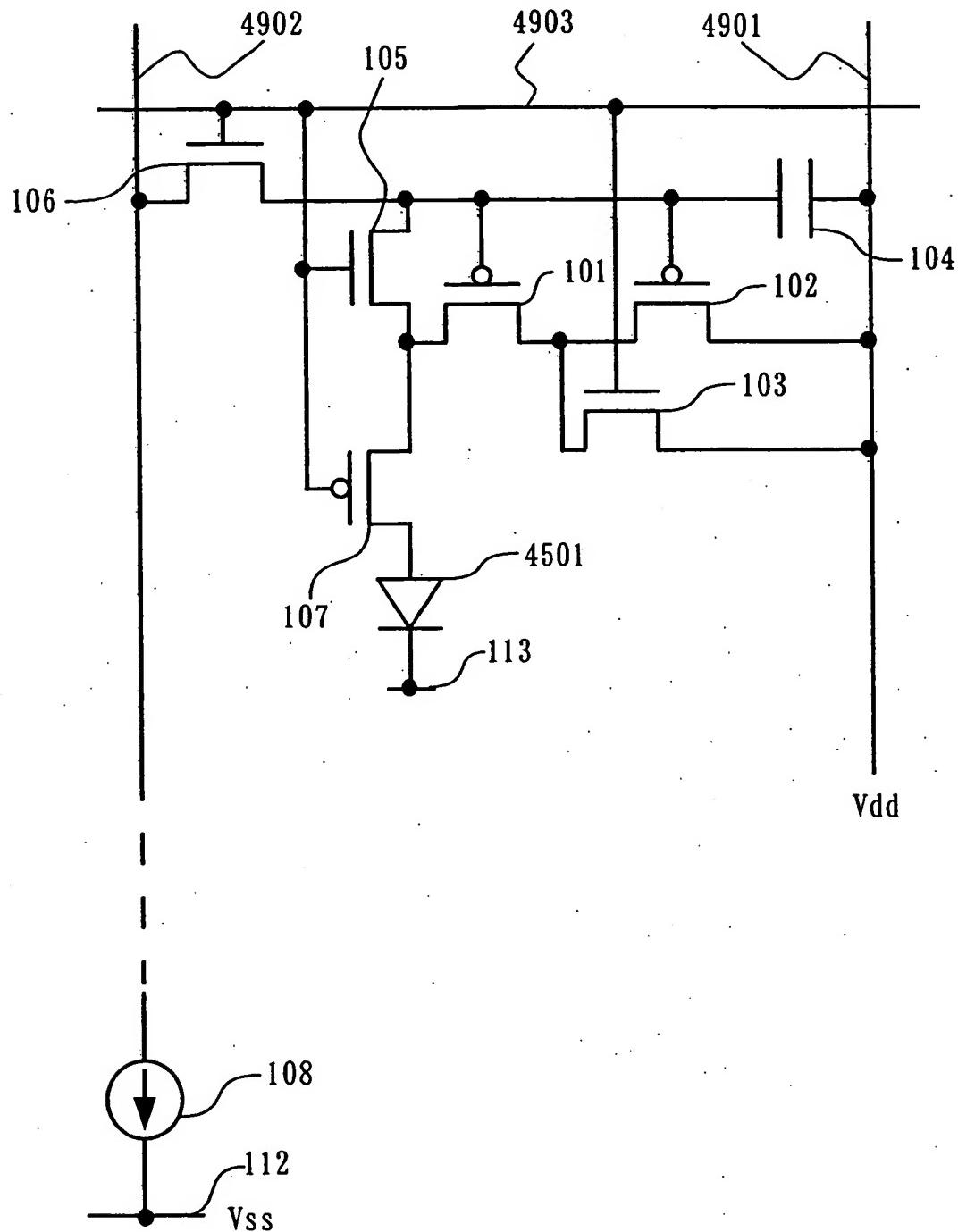
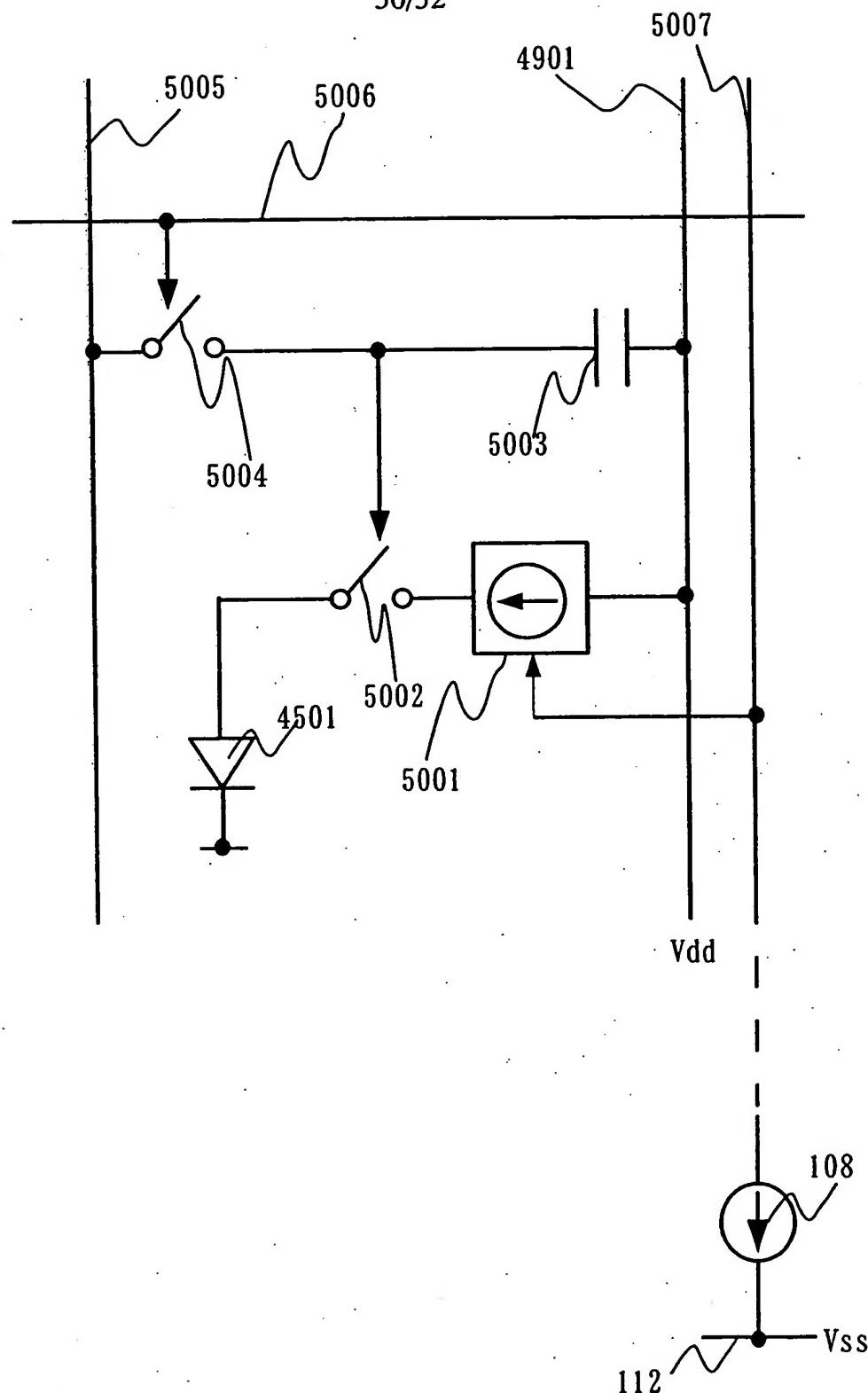


図49

50/50



四 50

51/52

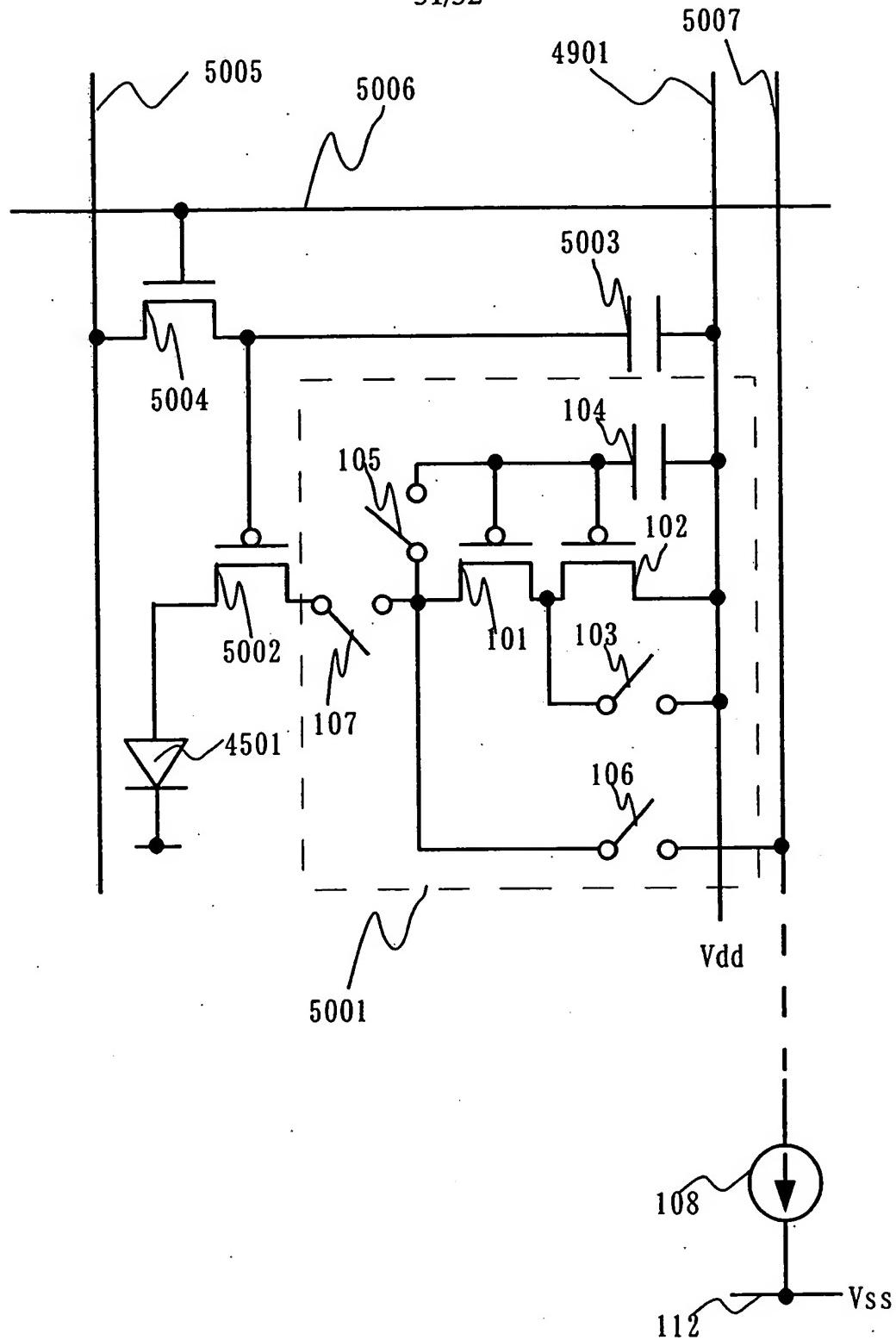


図51

52/52

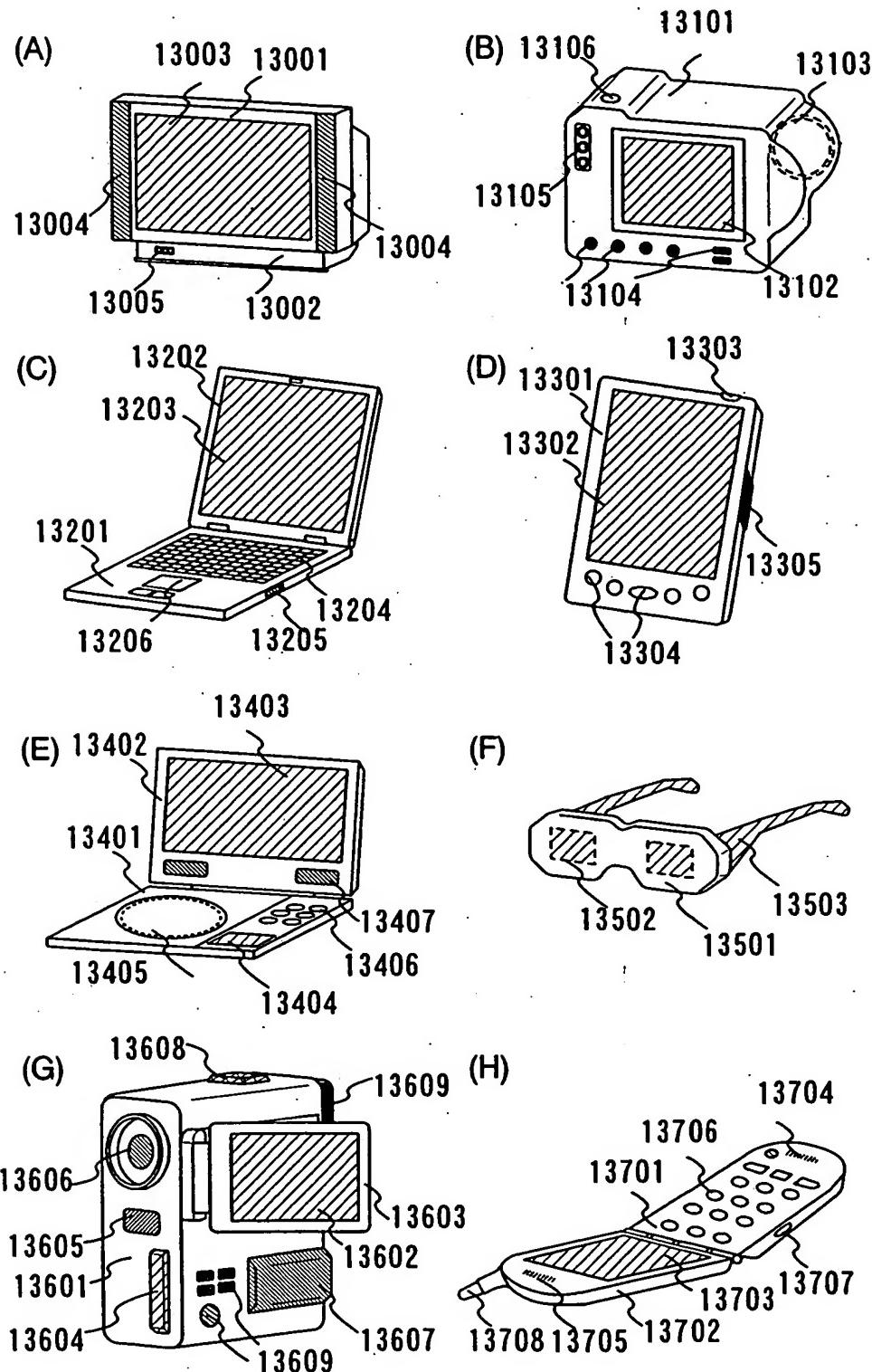


図 5 2